



Ruttonsha International Rectifier Ltd.

PHASE CONTROL THYRISTORS

HOCKEY PUCK VERSION

Type : 1300 PB 40 To 60

Features

- Center amplifying gate
- Metal case with ceramic insulator
- International standard case (B-PUK)
- High profile hockey-puk

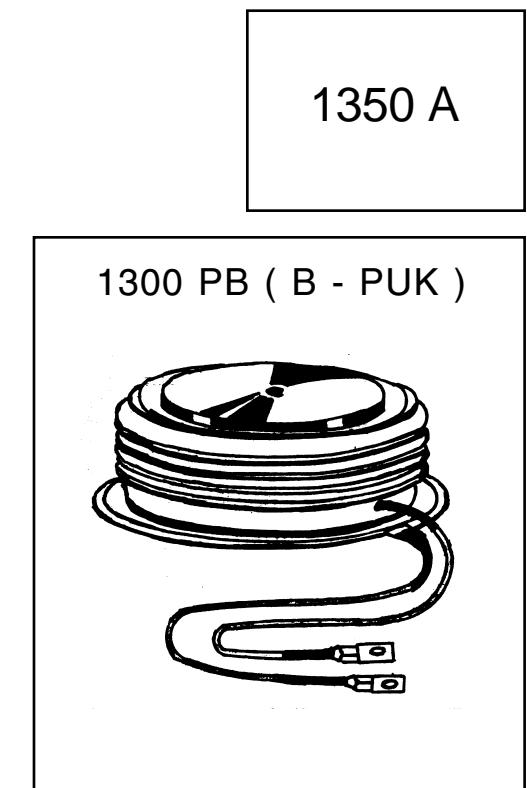
1350 A

Typical Applications

- D C motor controls
- Controlled D C power supplies
- A C controllers

Major Ratings and Characteristics :-

PARAMETERS	1300 PB	UNITS
$I_{T(AV)}$	1350	A
@ T_{hs}	55	°C
$I_{T(RMS)}$	2700	A
@ T_{hs}	25	°C
I_{TSM}	24400	A
I^2t @50Hz	2986	KA ² s
V_{DRM} / V_{RRM}	400 to 600	V
T_q typical	150	μs
T_J	- 40 to 125	°C



1300 PB (B - PUK)

PHASE CONTROL THYRISTORS

1300 PB Series

ELECTRICAL SPECIFICATIONS

Voltage Ratings

Type number	Voltage Code	V_{DRM}/V_{RRM} , max repetitive peak and off-state voltage V	V_{RSM} , maximum non-repetitive peak voltage V	I_{DRM}/I_{RRM} max. @ $T_J = T_{J\max}$ mA
1300 PB	40	400	500	80
	60	600	700	

On - state Conduction

Parameter	1300 PB	Units	Conditions				
$I_{T(AV)}$	Max. average on-state current	1350(500)	A	180° conduction, half sine wave double side (single side) cooled			
	@ Heatsink temperature	55 (85)	°C				
$I_{T(RMS)}$	Max RMS on-state current	2700		DC @ 25°C heatsink temperature double side cooled			
I_{TSM}	Max. peak, one-cycle non-repetitive surge current	24400	A	$t = 10 \text{ ms}$	No voltage reapplied	Sinusoidal half wave, Initial $T_J = T_{J\max}$.	
I^2t	Maximum I^2t for fusing	2986	KA ² s	$t = 10 \text{ ms}$	No voltage reapplied		
$V_{T(TO)}$	Threshold voltage	0.80	V	$T_J = T_{J\max}$			
r_t	On-state slope resistance	0.26	mΩ	$T_J = T_{J\max}$			
V_{TM}	Max. on state voltage	1.31	V	$I_{PK} = 3600A, T_J = T_{J\max}, t_P = 10 \text{ ms}$ sine pulse			
I_H	Maximum holding current	600	mA	$T_J = 25^\circ\text{C}$, anode supply 12 V resistive load			
I_L	Typical latching current	1000					

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Switching

1300 PB Series

Parameter	1300 PB	Units	Conditions
di/dt Max. non-repetitive rate of rise of turned-on current	100	A/μs	Gate drive 20V, 20Ω, $t_r \leq 1\mu s$ $T_J = T_{J\max}$, anode voltage ≤ 80% V_{DRM}
t_d Typical delay time	1.0	μs	Gate current 1A, $di_g/dt = 1A/\mu s$ $V_d = 0.67\% V_{DRM}$, $T_J = 25^\circ C$
t_q Typical turn-off time	150		$I_{TM} = 750A$, $T_J = T_{J\max}$, di/dt = 60A/μs, $V_R = 50V$ dv/dt = 20V/μs, Gate OV 100 Ω, $t_p = 500\mu s$

Blocking

Parameter	1300 PB	Units	Conditions
dv/dt Maximum critical rate of rise of off-state voltage	500	V/μs	$T_J = T_{J\max}$ linear to 80% rated V_{DRM}
I_{RRM} Max. peak reverse and off-state leakage current	80	mA	$T_J = T_{J\max}$ rated V_{DRM} / V_{RRM} applied

Triggering

Parameter	1300 PB	Units	Conditions
P_{GM} Maximum peak gate power	10.0	W	$T_J = T_{J\max}$, $t_p \leq 5 ms$
$P_{G(AV)}$ Maximum average gate power	2.0		$T_J = T_{J\max}$, $f = 50Hz$, $d\% = 50$
I_{GM} Max. peak positive gate current	3.0	A	$T_J = T_{J\max}$, $t_p \leq 5 ms$
$+V_{GM}$ Maximum peak positive gate voltage	20	V	$T_J = T_{J\max}$, $t_p \leq 5 ms$
$-V_{GM}$ Maximum peak negative gate voltage	5.0		
I_{GT} DC gate voltage required to trigger	200	mA	$T_J = 25^\circ C$ Max. required gate trigger/ current/voltage are the lowest value which will trigger all units 12 V anode-to-cathode applied
V_{GT} DC gate voltage required to trigger	3.0	V	$T_J = 25^\circ C$
I_{GD} DC gate current not to trigger	10	mA	$T_J = T_{J\max}$
V_{GD} DC gate voltage not to trigger	0.25	V	

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Thermal and Mechanical Specification

Parameter	1300 PB	Units	Conditions
T _J	Max.operating temperature range	°C	
T _{stg}	Max.storage temperature range		
R _{thJ-hs}	Max. thermal resistance, junction to heatsink	0.073	DC operation single side cooled DC operation double side cooled
		0.031	
R _{thC-hs}	Max. thermal resistance, case to heatsink	0.011	DC operation single side cooled DC operation double side cooled
		0.006	
F	Mounting force, ± 10%	14700 (1500)	N (Kg.)
wt	Approximate weight	255	g
	Case style	TO-200AC (B-PUK)	See Outline table

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1300 PB Series

Outline Table

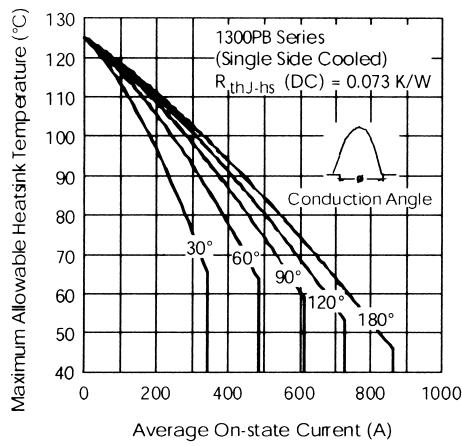
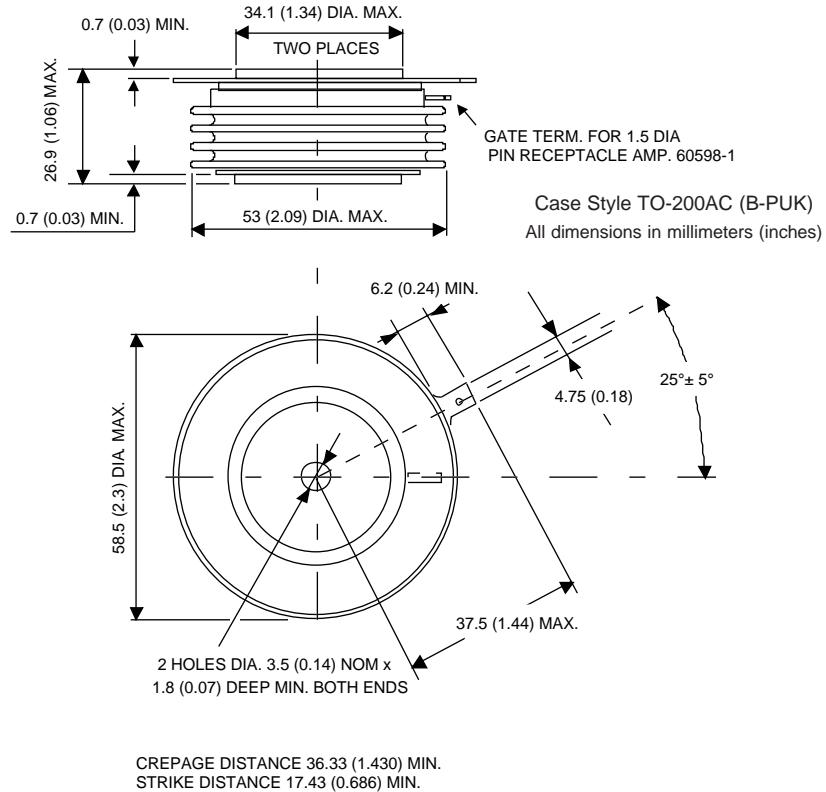


Fig. 1 - Current Ratings Characteristics

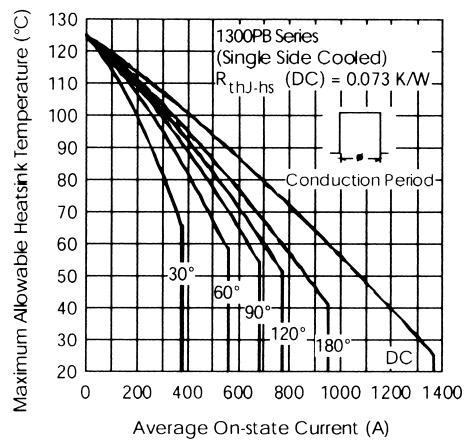


Fig. 2 - Current Ratings Characteristics

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1300 PB Series

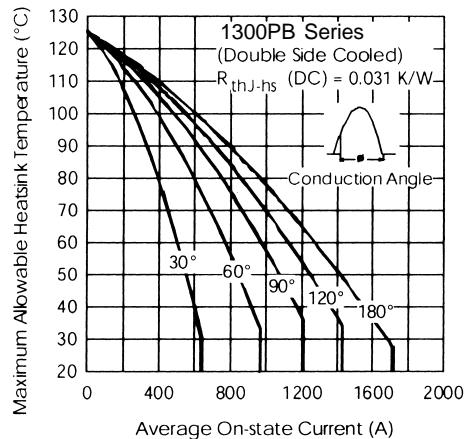


Fig. 3 - Current Ratings Characteristics

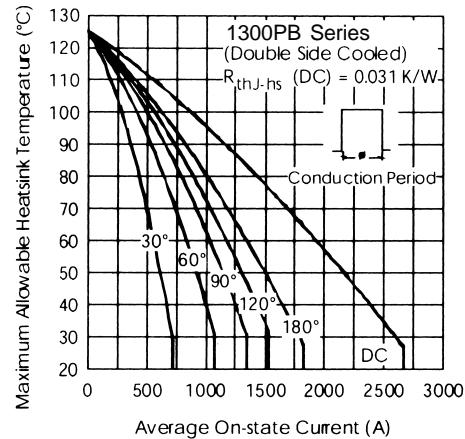


Fig. 4 - Current Ratings Characteristics

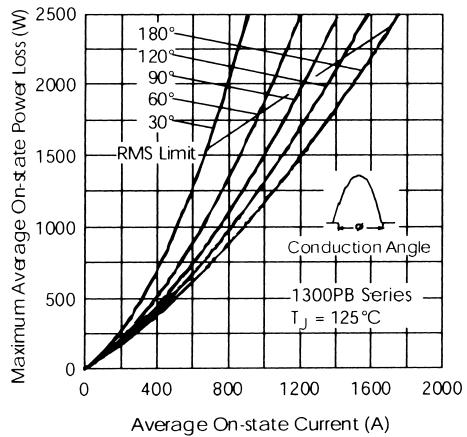


Fig. 5 - On-state Power Loss Characteristics

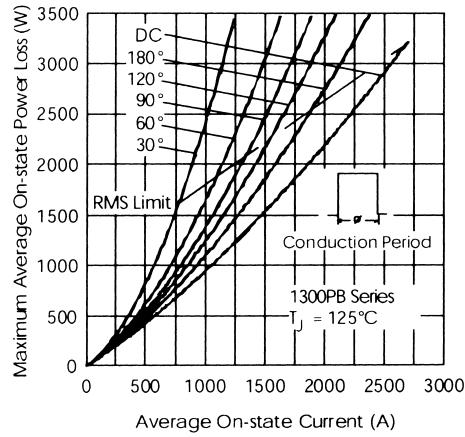


Fig. 6 - On-state Power Loss Characteristics

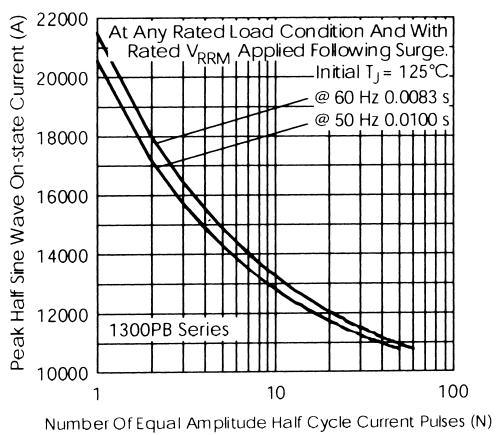


Fig. 7 - Maximum Non-Repetitive Surge Current
Single and Double Side Cooled

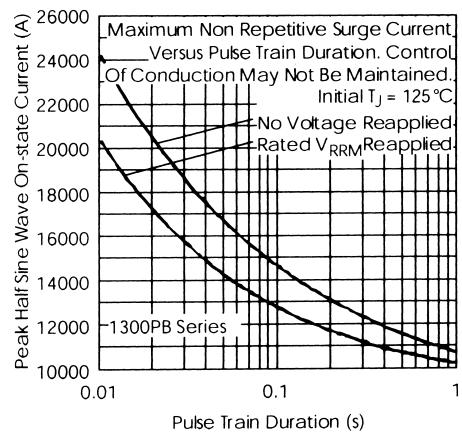


Fig. 8 - Maximum Non-Repetitive Surge Current
Single and Double Side Cooled

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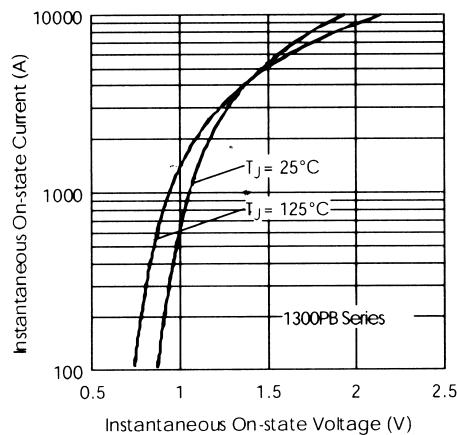


Fig. 9 - On-state Voltage Drop Characteristics

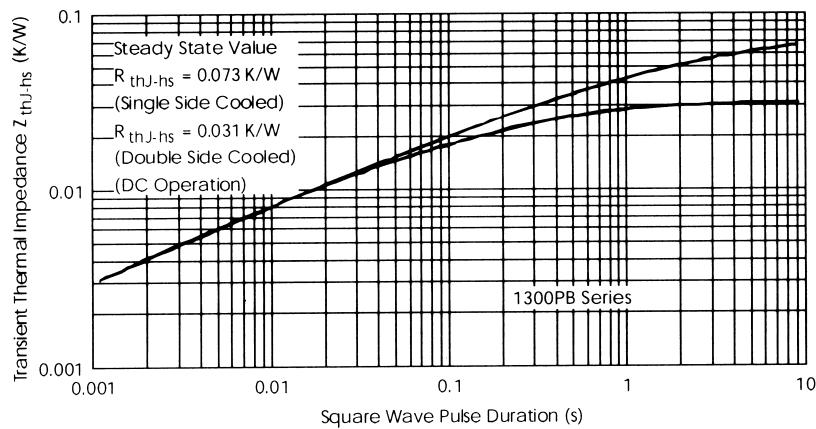


Fig. 10 - Thermal Impedance Z_{thJ-hs} Characteristics

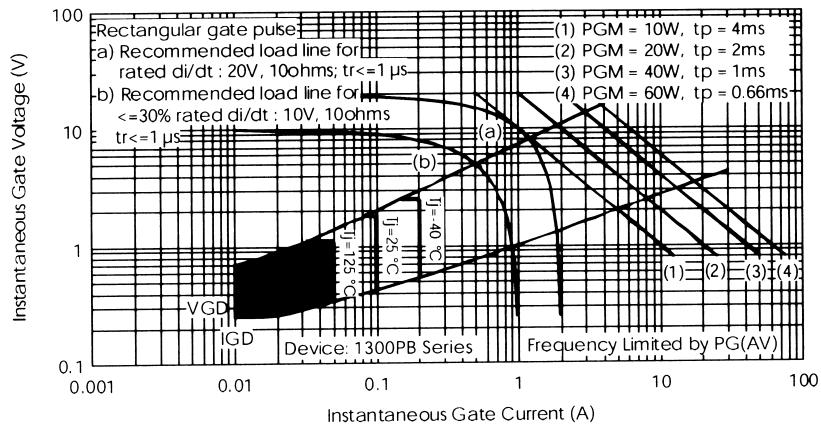


Fig. 11 - Gate Characteristics