



Ruttonsha International Rectifier Ltd.

PHASE CONTROL THYRISTORS

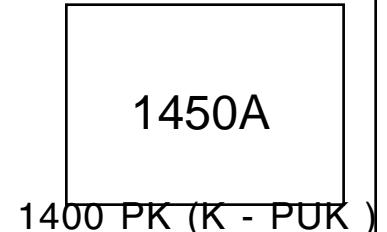
HOCKEY PUK VERSION

Type : 1450 PK 120 To 240

Features

- Center amplifying gate
- Metal case with ceramic insulator
- International standard case (K-PUK)
- High profile hockey-puk

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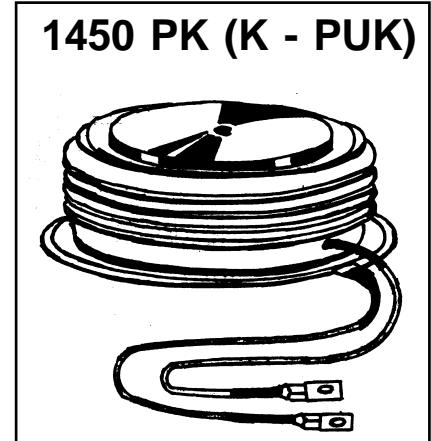


Typical Applications

- D C motor controls
- Controlled D C power supplies
- A C controllers

Major Ratings and Characteristics :-

PARAMETERS	1450 PK	UNITS
$I_{T(AV)}$	1450	A
@ T_{hs}	55	°C
$I_{T(RMS)}$	2276	A
@ T_{hs}	55	°C
I_{TSM} @50Hz	19.4	KA
I^2t @50Hz	1880	KA ² s
$I^2\sqrt{t}$	18800	KA ² √s
V_{DRM} / V_{RRM}	UP TO 2400	V
T_q typical	250	μs
T_J	- 40 to 125	°C



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ELECTRICAL SPECIFICATIONS

1450 PK Series

Voltage Ratings

Type number	Voltage Code	V_{DRM}/V_{RRM} , max repetitive peak and off-state voltage V	V_{RSM} , maximum non-repetitive peak voltage V	I_{DRM}/I_{RRM} max. @ $T_J = T_{J\max}$ mA
1450 PK	120	1200	1250	100
	160	1600	1650	
	200	2000	2050	
	220	2200	2250	
	240	2400	2450	

On - state Conduction

Parameter	1450 PK	Units	Conditions		
$I_{T(AV)}$	Max. average on-state current	1450	A	180° conduction, half sine wave double side cooled	
	@ Heatsink temperature	55	°C		
I_{TSM}	Max. peak, one-cycle non-repetitive surge current	19.4	KA	t = 10 ms	No voltage reapplied
I^2t	Maximum I^2t for fusing	1880	KA ² s	t = 10 ms	No voltage reapplied
$V_{T(TO)}$	Value of threshold voltage	1.0 max.	V	$T_{VJ} = T_{VJ\max}$	
r_t	Value of on-state slope resistance	0.28	mΩ	$T_{VJ} = T_{VJ\max}$	
I_{RM}	Pak reverse recovery current	50 max.	A	$T_{VJ} = T_{VJ\max}$, $iTM = ITAVM$, $-dT/dt = 2A/\mu s$, $VR = 0.5V_{RRM}$, $VRM = 0.8V_{RRM}$	
V_{TM}	Max. on state voltage drop	1.30	V	$I_{PK} = 1000A$, $T_J = 125^\circ C$	
I_H	Maximum holding current	500	mA	$T_J = 25^\circ C$, $v_D = 6 V$, $R_A = 5\Omega$	
I_L	Typical latching current	1500	mA	$T_J = 25^\circ C$, $v_D = 6 V$, $R_{GK} \geq 10\Omega$ $I_{GM} = 3A$, $dI/dt = 6A/\mu s$, $T_q = 20\mu s$	

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Switching

1450 PK Series

Parameter	1450 PK	Units	Conditions
di/dt Max. non-repetitive rate of rise of turned-on current	100	A/ μ s	Gate drive 20V, 20Ω , $t_r \leq 1\mu$ s $T_J = T_{J\max}$, anode voltage $\leq 80\% V_{DRM}$
t_d Typical delay time	1.5	μ s	Gate current 1A, $di_g/dt = 1A/\mu$ s $V_d = 0.67\% V_{DRM}$, $T_J = 25^\circ C$
t_q Typical turn-off time	250	μ s	$T_{vj} = T_{vj\max} 1000A$, $i_{TM} = i_{TAVM}$ $v_{RM} = 100 V$, $v_{DM} = 0.67 V_{DRM}$ $dvD/dt = 20 V/\mu$ s, $-dT/dt = 10A/\mu$ s

Blocking

Parameter	1450 PK	Units	Conditions
dv/dt Maximum critical rate of rise of off-state voltage	500	V/ μ s	$T_J = T_{J\max}$, $V_D = 0.67 V_{DRM}$
I_{RRM} Max. peak reverse and off-state leakage current	100	mA	$T_J = T_{J\max}$, rated V_{DRM} , V_{RRM} applied

Triggering

Parameter	1450 PK	Units	Conditions
I_{GT} DC gate current required to trigger	250 Max.	mA	$T_J = 25^\circ C$, $V_D = 10V$
V_{GT} DC gate voltage required to trigger	3.0 Max.	V	$T_J = 25^\circ C$, $V_D = 10V$
I_{GD} Gate non-trigger current	20 Max.	mA	$T_{VJ} = T_{VJ\max}$, $V_D = 6 V_{DRM}$
	10 Max.	mA	$T_{VJ} = T_{VJ\max}$, $V_D = 0.5 V_{DRM}$
V_{GD} Gate non-trigger voltage	0.4 Max.	V	$T_{VJ} = T_{VJ\max}$, $V_D = 0.5 V_{DRM}$

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1450 PK Series

Thermal and Mechanical Specifications

Parameter	1450 PK	Units	Conditions
T_J	Max.operating temperature range - 40 to 125	$^{\circ}\text{C}$	
T_{stg}			
$R_{\text{thJ-hs}}$	Max. thermal resistance, junction to heatsink	0.024	K/W DC operation double side cooled
$R_{\text{thC-hs}}$	Max. thermal resistance, case to heatsink	0.006	DC operation double side cooled
		0.003	
F	Mounting force, $\pm 10\%$ 24500 (2500)	N (Kg.)	
wt	Approximate weight 550	g	
Case style	A-24(K-PUK)	See Outline Table	

Outline Table

