

High Power Thyristor Hockey Puk Version K-PUK Series 1700PK

Types : 1700PK 60 to 1700PK 220

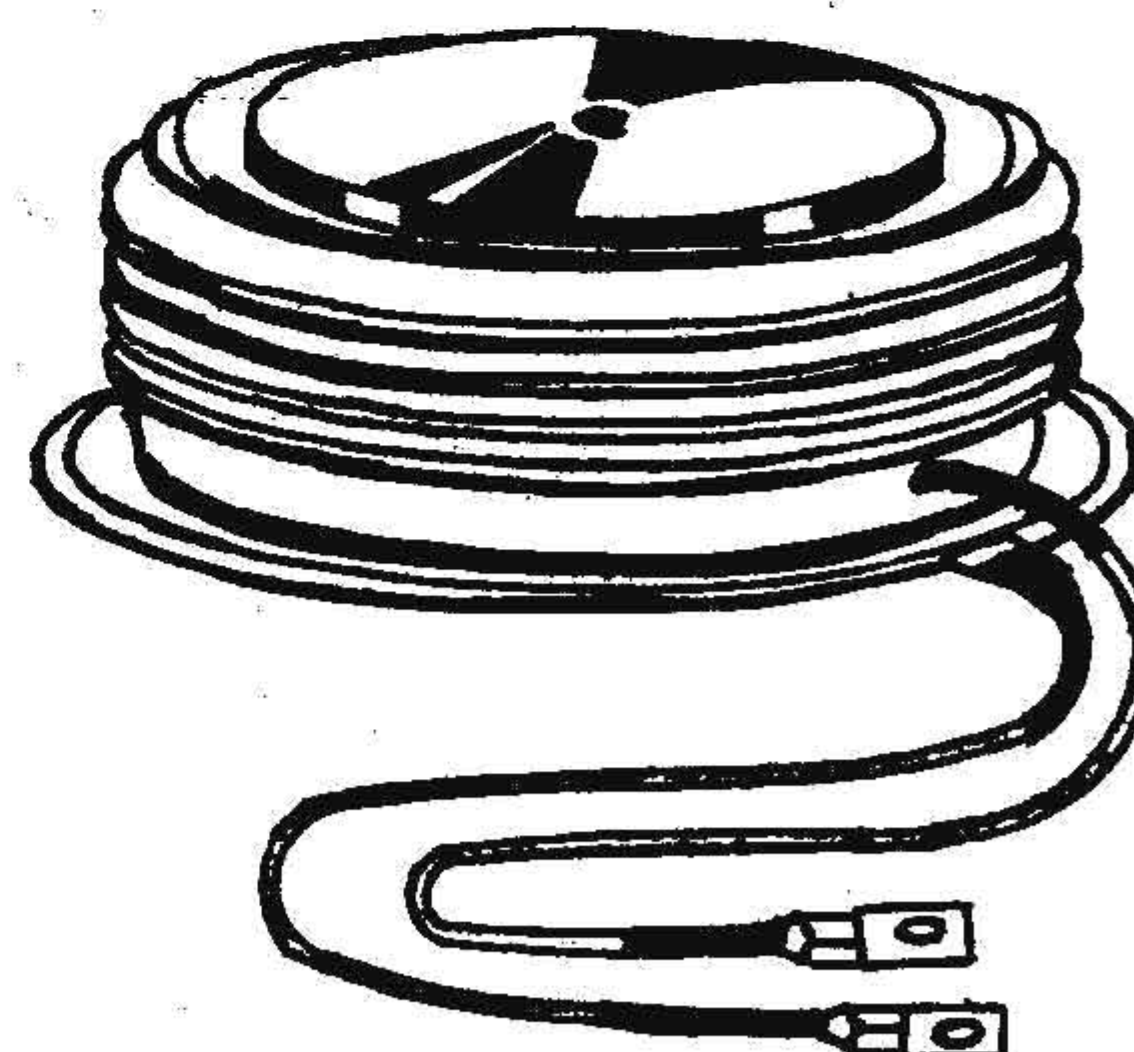
FEATURES

- ❖ Center amplifying gate.
- ❖ Metal case with ceramic insulator
- ❖ International standard case A-24.
- ❖ High profile hockey - puk.

TYPICAL APPLICATIONS

- ❖ DC motor control (e.g. for machine tools).
- ❖ Controlled rectifiers (e.g. for battery charging, Uninterrupted Power Supply).
- ❖ AC controllers (e.g. for temperature control, lights control).

1700 PK (K - PUK)



MAJOR RATINGS & CHARACTERISTICS

Parameters	1700PK	Units
$I_{T(AV)}$	1745	A
@ T_{hs}	55	$^{\circ}C$
$I_{T(RMS)}$	2740	A
@ T_{hs}	55	$^{\circ}C$
I_{TSM} @ 50 Hz	33500	A
I^2t @ 50 Hz	5615	KA ² s
V_{DRM} / V_{RRM}	600 to 2200	V
t_q typical	200	μs
T_J	-40 to 125	$^{\circ}C$

SILICON CONTROLLED RECTIFIERS

1700PK

ELECTRICAL SPECIFICATION VOLTAGE RATINGS

Type Number	Voltage Code	V_{RRM} / V_{DRM} max. repetitive peak and off-state voltage V	V_{RSM} max. non-repetitive peak voltage V	I_{DRM} / I_{RRM} max. @ 125°C mA
1700PK	60	600	700	100
	80	800	900	
	100	1000	1100	
	120	1200	1300	
	140	1400	1500	
	160	1600	1700	
	180	1800	1900	
	200	2000	2100	
	220	2200	2300	

ON-STATE CONDUCTION

	Parameter	1700PK	Units	Conditions
$I_{T(AV)}$	Max. average on-state current @ heat sink temperature	1745	A	180° conduction, half sine wave double side cooled
		55	°C	
$I_{T(RMS)}$	Max. RMS on-state current	2740		@55°C heat sink temperature (double side cooled)
I_{TSM}	Max. peak one cycle non-repetitive surge current	33500	A	t = 10ms
I^2t	Maximum I^2t for fusing	5615	kA ² s	Sinusoidal half wave, Initial $T_J = T_J$ max.
$V_{T(TO)}$	Threshold voltage	0.93	V	$T_J = T_J$ max.
r_t	On state slope resistance	0.17	mΩ	$T_J = T_J$ max.
V_{TM}	Max. on state voltage	1.85	V	$I_{pk} = 4000A$, $T_J = T_J$ max., $t_p = 10ms$ sine pulse
I_H	Maximum holding current	600	mA	$T_J = 25°C$, anode supply 12V resistive load
I_L	Latching current	1000		

SWITCHING

	Parameter	1700PK	Units	Conditions
di/dt	Max. non-repetitive rate of rise of turned-on current	100	A/μs	Gate drive 20V, 20Ω, $t_r \leq 1 \mu s$ $T_J = T_J$ max., anode voltage $\leq 80\% V_{DRM}$
t_d	Typical delay time	2.0	μs	Gate current 1A, $di_g/dt = 1A/\mu s$ $V_d = 0.67\% V_{DRM}$, $T_J = 25°C$
t_q	Typical turn-off time	200		

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BLOCKING

	Parameter	1700PK	Units	Conditions
dv/dt	Maximum critical rate of rise of off-state voltage	500	V/ μ s	$T_J = T_J \text{ max.}$, linear to 60% rated V_{DRM}
I_{RRM} I_{DRM}	Max. peak reverse and off-state leakage current	100	mA	$T_J = T_J \text{ max.}$, rated V_{DRM} / V_{RRM} applied

TRIGGERING

	Parameter	1700PK	Units	Conditions
P_{GM}	Maximum peak gate power	16.0	W	$T_J = T_J \text{ max.}$, $t_p \leq 5\text{ms}$
$P_{\text{G(AV)}}$	Maximum average gate power	3.0		$T_J = T_J \text{ max.}$, $f = 50\text{Hz}$, $d\% = 50$
I_{GM}	Max. peak positive gate current	3.0	A	$T_J = T_J \text{ max.}$, $t_p \leq 5\text{ms}$
$+V_{\text{GM}}$	Max. peak positive gate voltage	20	V	$T_J = T_J \text{ max.}$, $t_p \leq 5\text{ms}$
$-V_{\text{GM}}$	Max. peak negative gate voltage	5.0		
I_{GT}	DC gate current required to trigger	250	mA	$T_J = -40^\circ\text{C}$ $T_J = 25^\circ\text{C}$ $T_J = 125^\circ\text{C}$ Max. required gate trigger/current / voltage are the lowest value which will trigger all units 12V anode-to-cathode applied.
V_{GT}	DC gate voltage required to trigger	3.0	V	
I_{GD}	DC gate current not to trigger	10	mA	$T_J = T_J \text{ max.}$ Max. gate current / voltage not to trigger is the max. value which will not trigger any unit with rated V_{DRM} anode-to-cathode applied.
V_{GD}	DC gate voltage not to trigger	0.25	V	

THERMAL AND MECHANICAL SPECIFICATION

	Parameter	1700PK	Units	Conditions
T_J	Max. operating temperature range	-40 to 125	$^\circ\text{C}$	
T_{stg}	Max. storage temperature range	-40 to 150		
$R_{\text{thJ-hs}}$	Max. thermal resistance, junction to heat sink	0.024	K/W	DC operation double side cooled
F	Mounting force, $\pm 10\%$	24500 (2500)	N (kg)	
wt	Approximate weight	425	g	
	Case style	A-24 (K-PUK)		See outline

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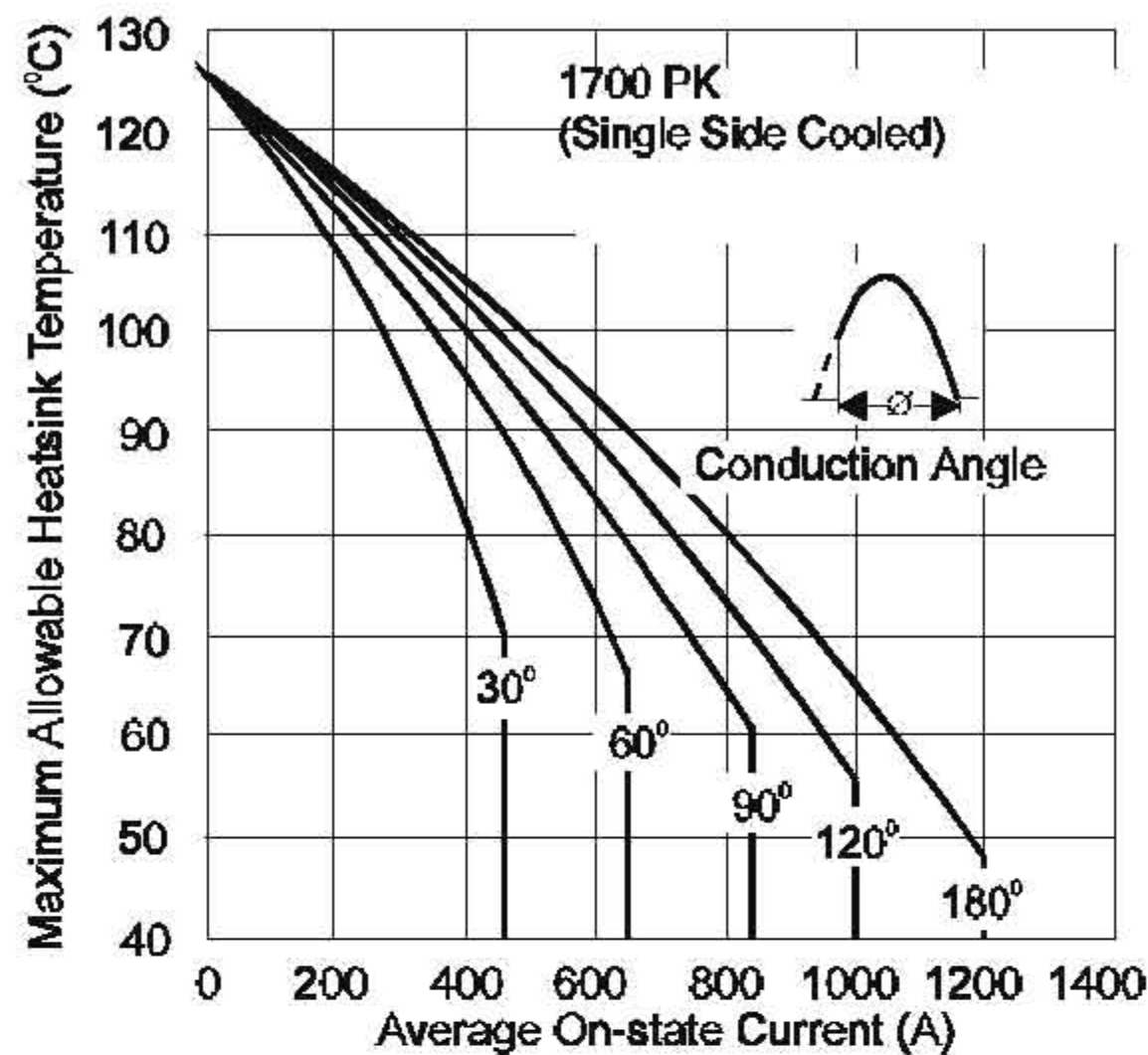


Fig. 1 - Current Ratings Characteristics

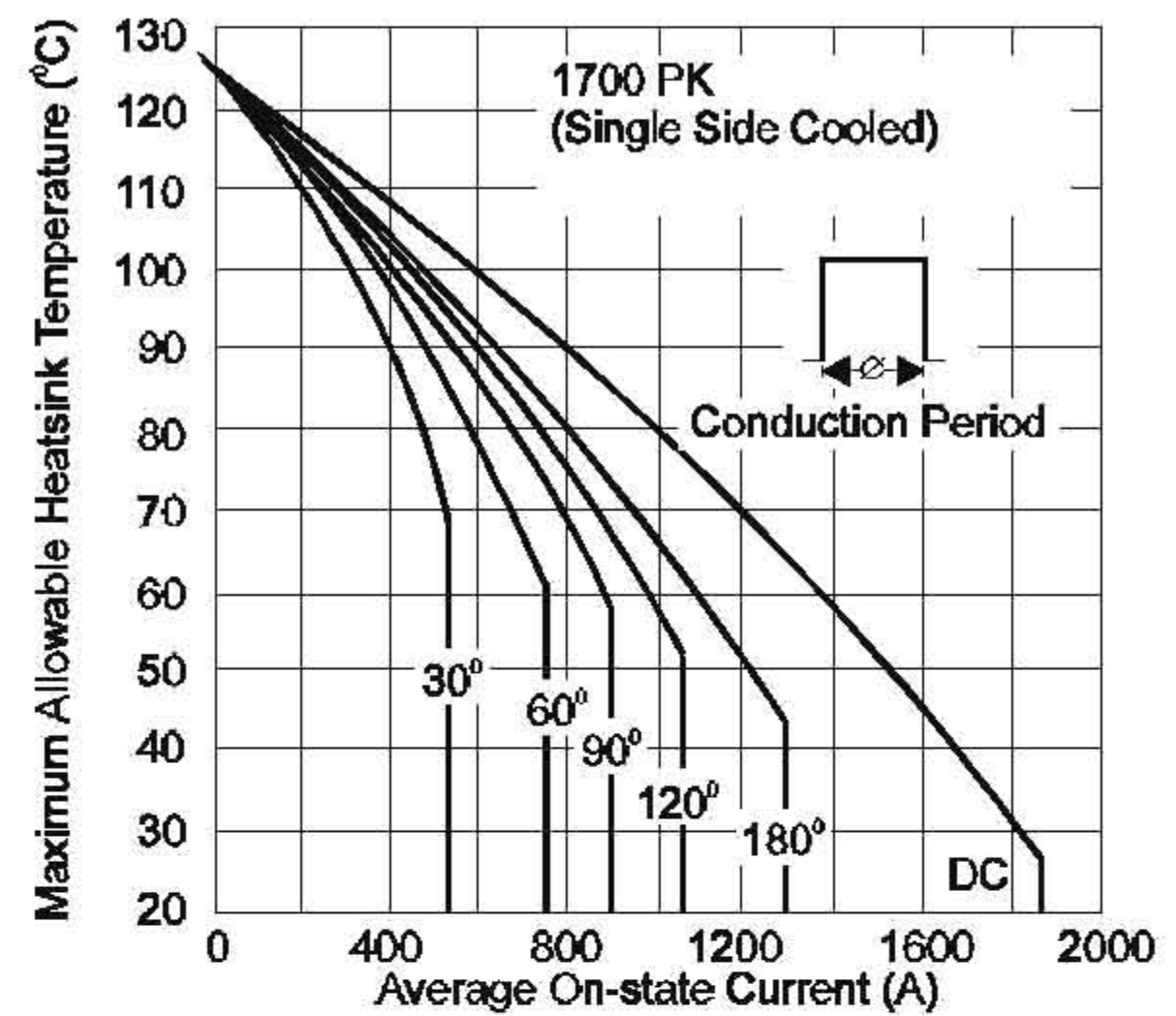


Fig. 2 - Current Ratings Characteristics

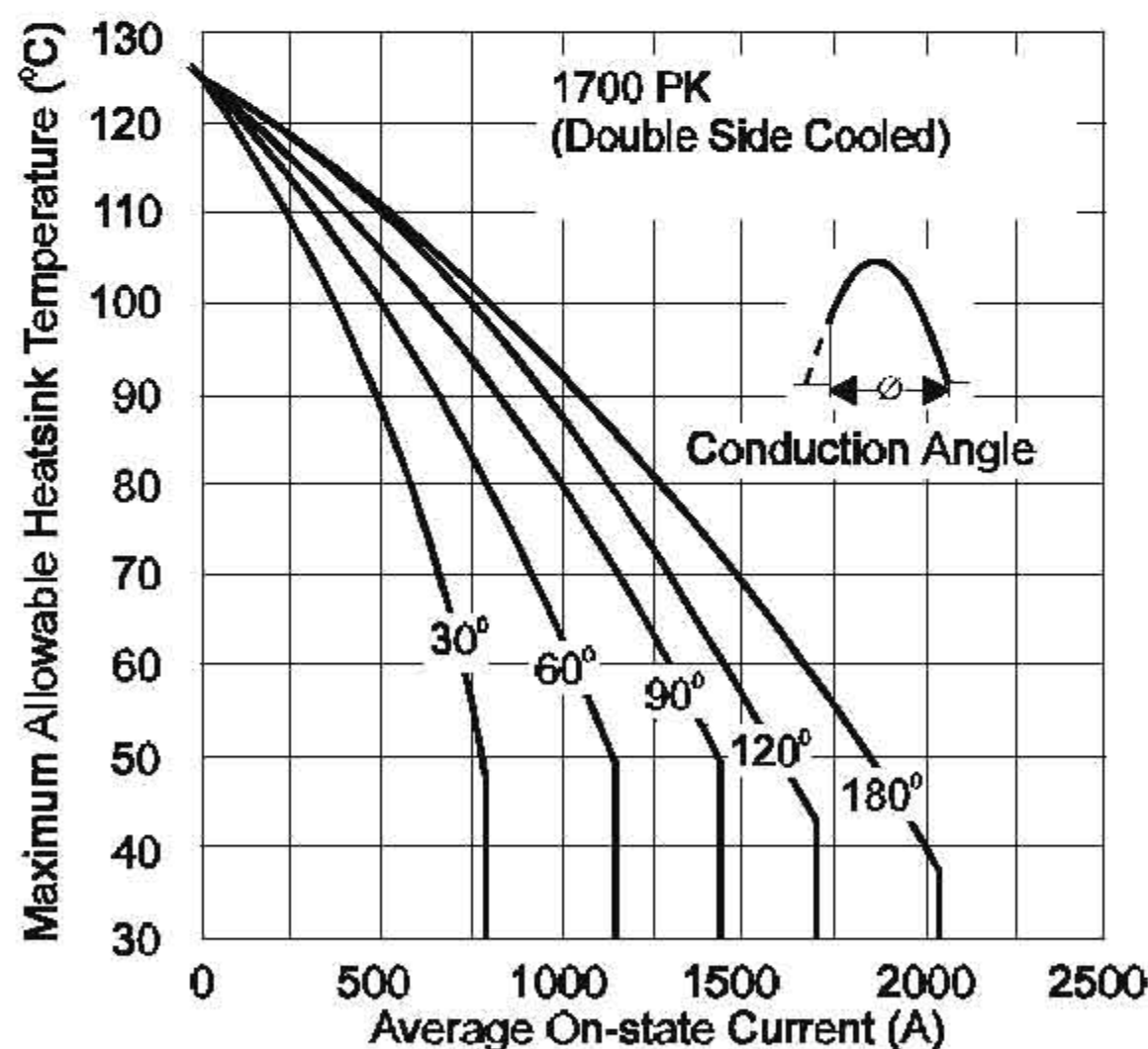


Fig. 3 - Current Ratings Characteristics

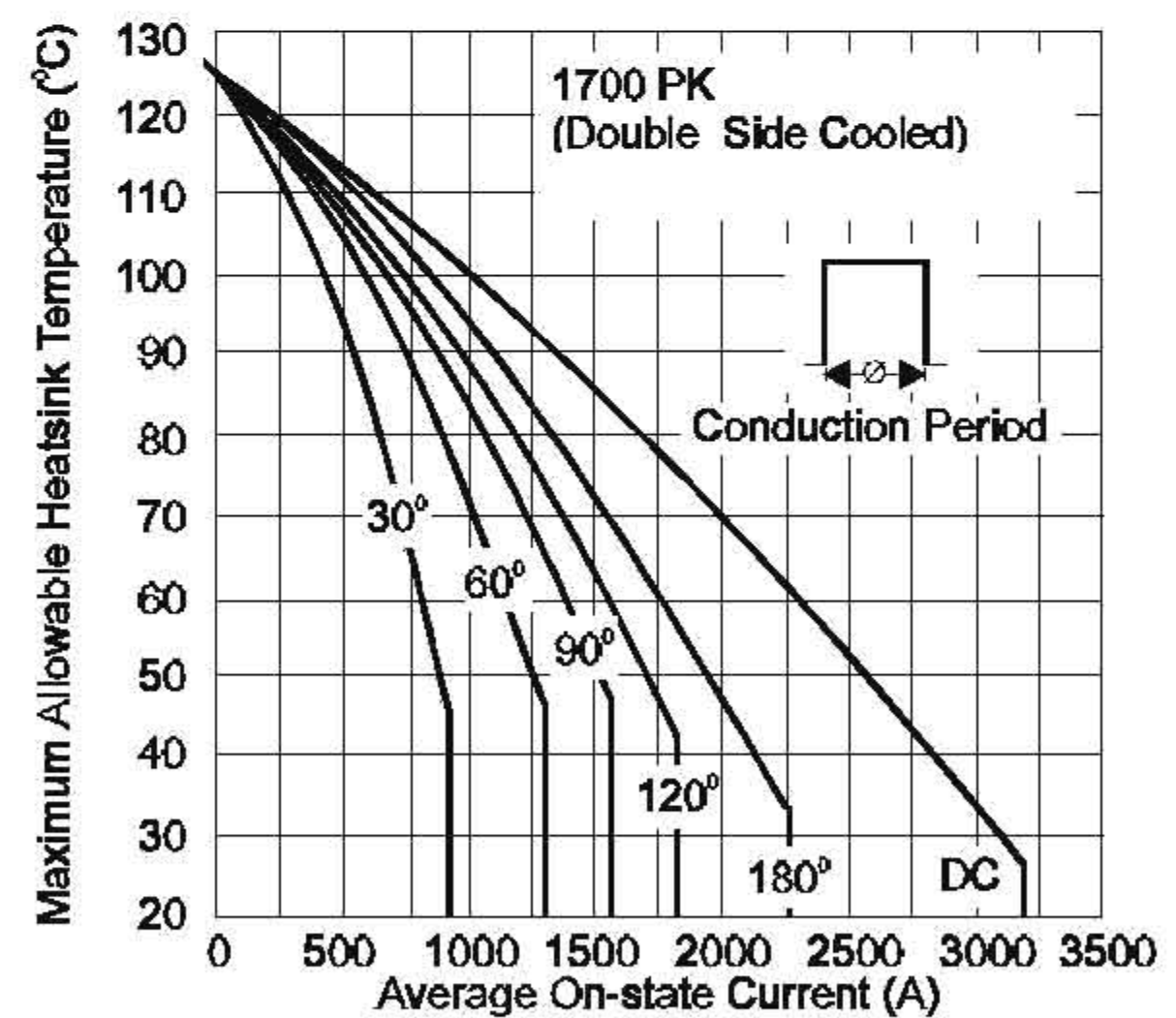


Fig. 4 - Current Ratings Characteristics

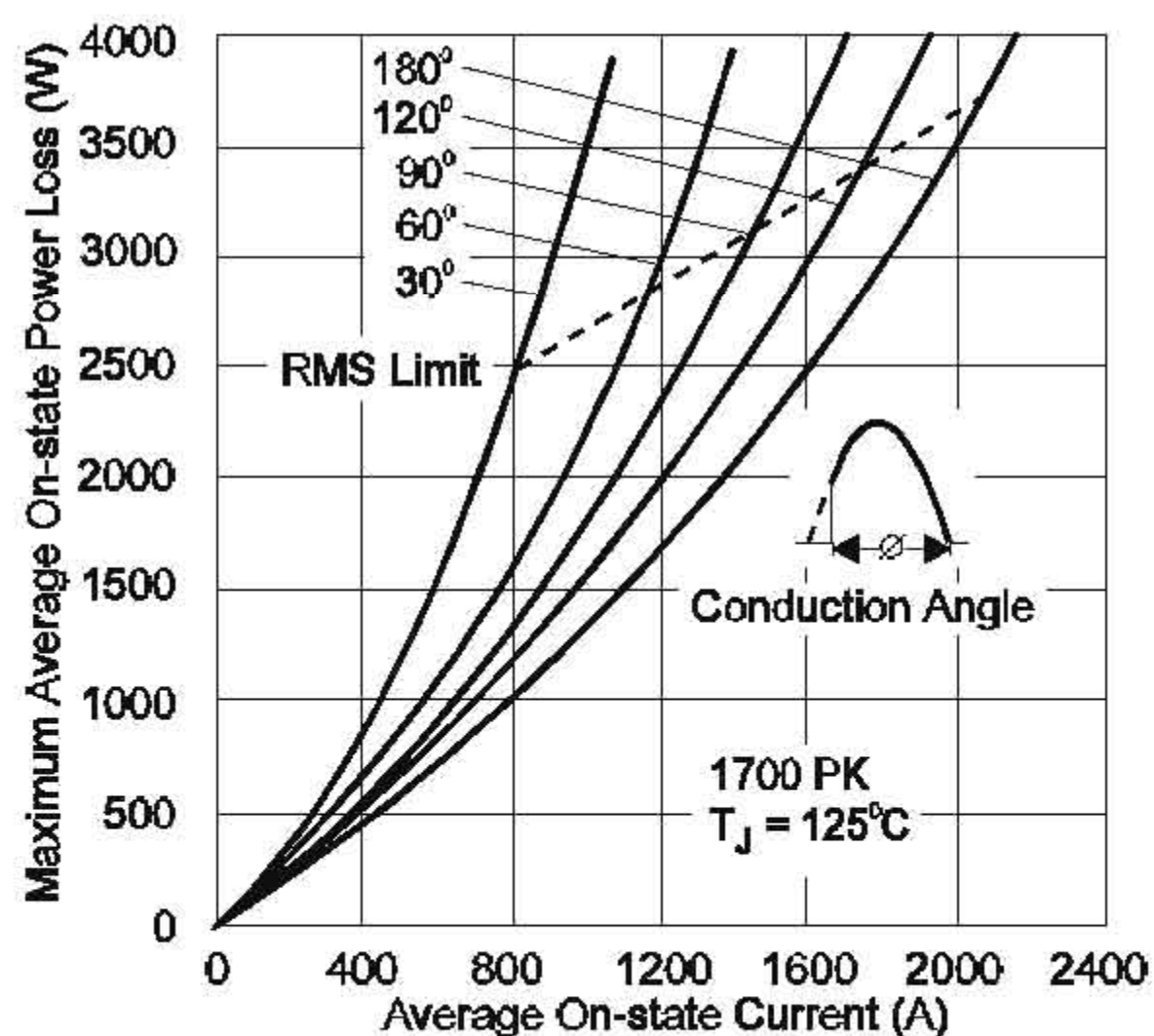


Fig. 5 - On-state Power Loss Characteristics

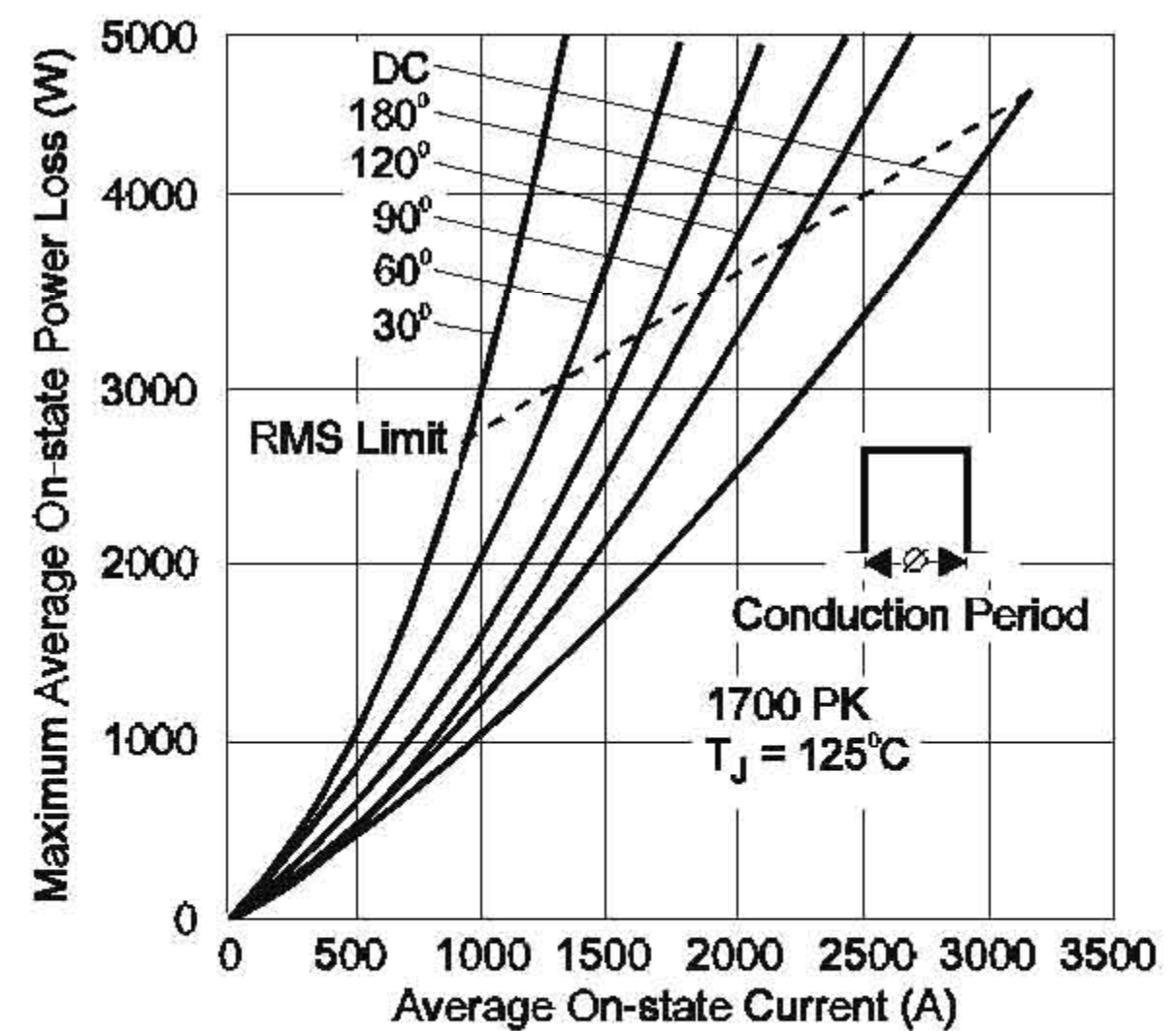


Fig. 6 - On-state Power Loss Characteristics

SILICON CONTROLLED RECTIFIERS

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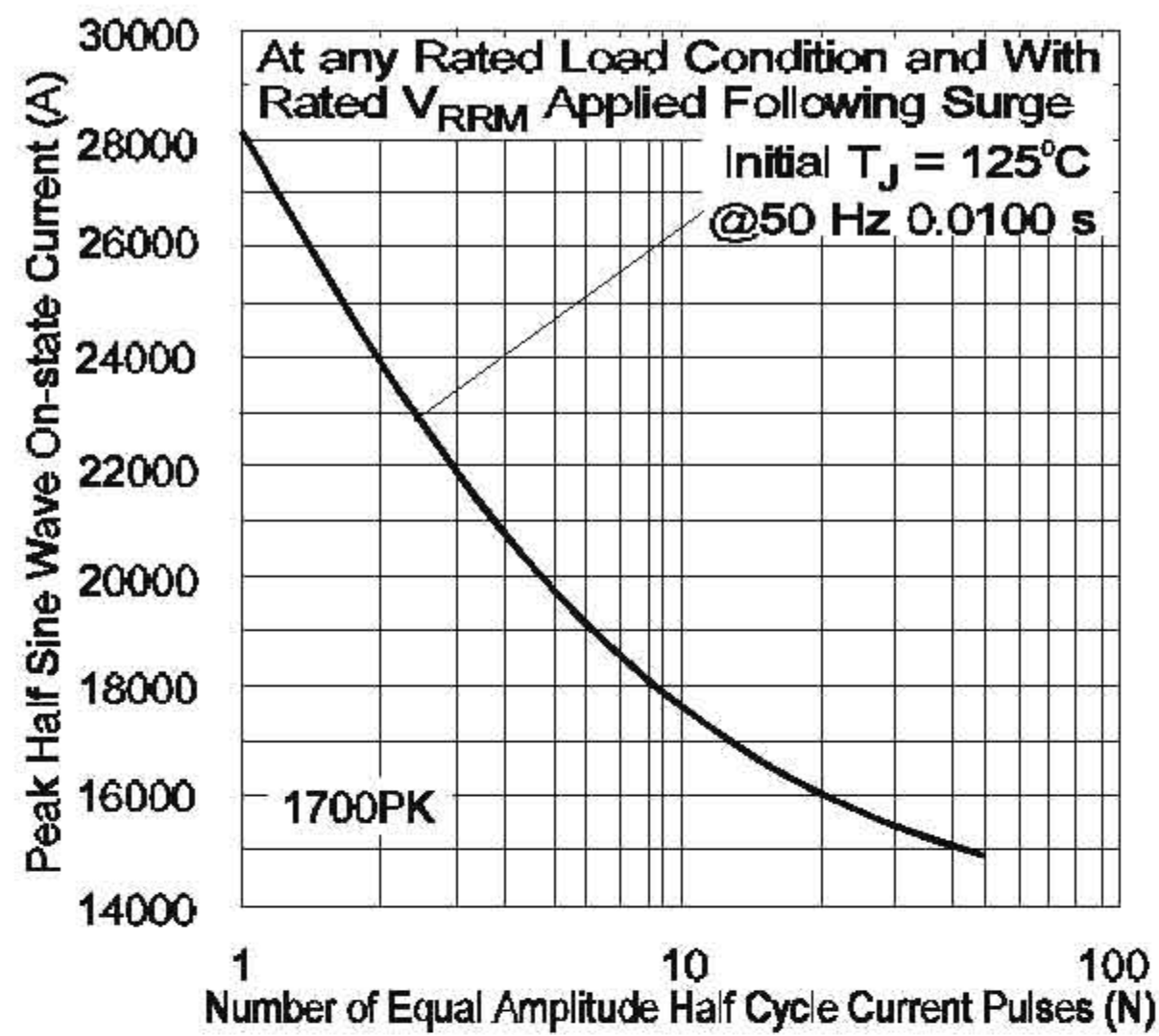


Fig. 7 - Maximum Non-Repetitive Surge Current

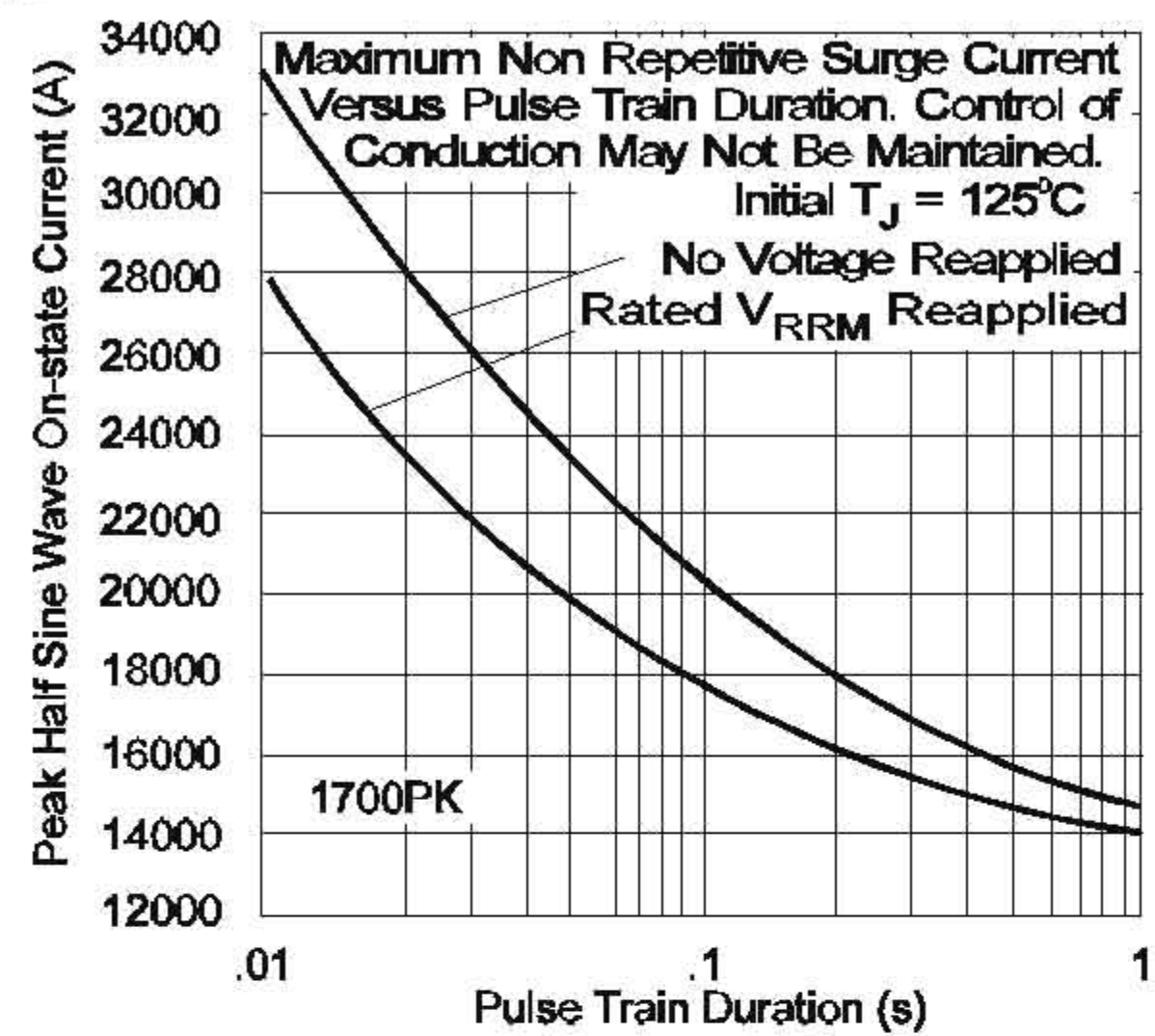


Fig. 8 - Maximum Non-Repetitive Surge Current

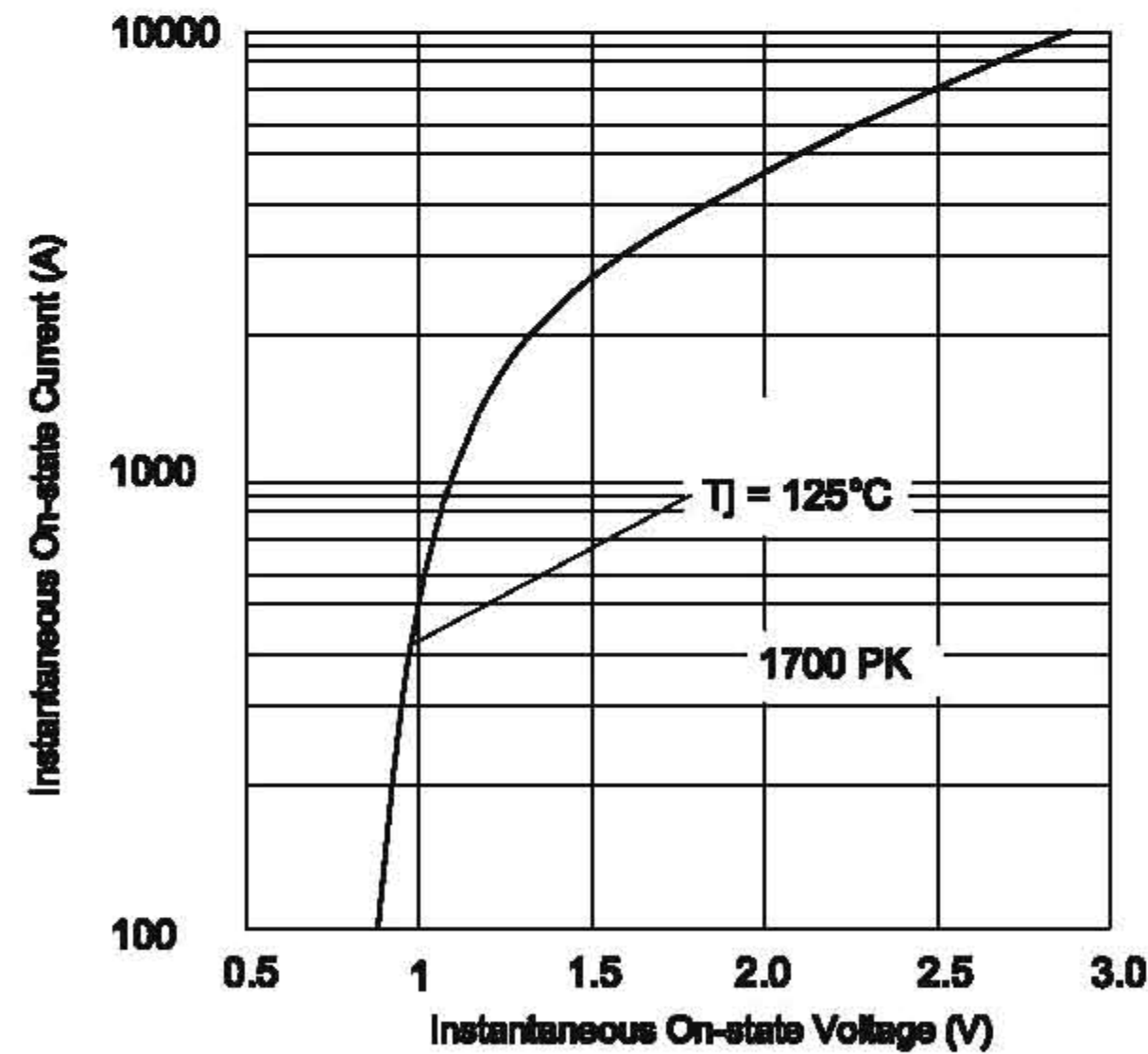


Fig. 9 - On-state Voltage Drop Characteristics

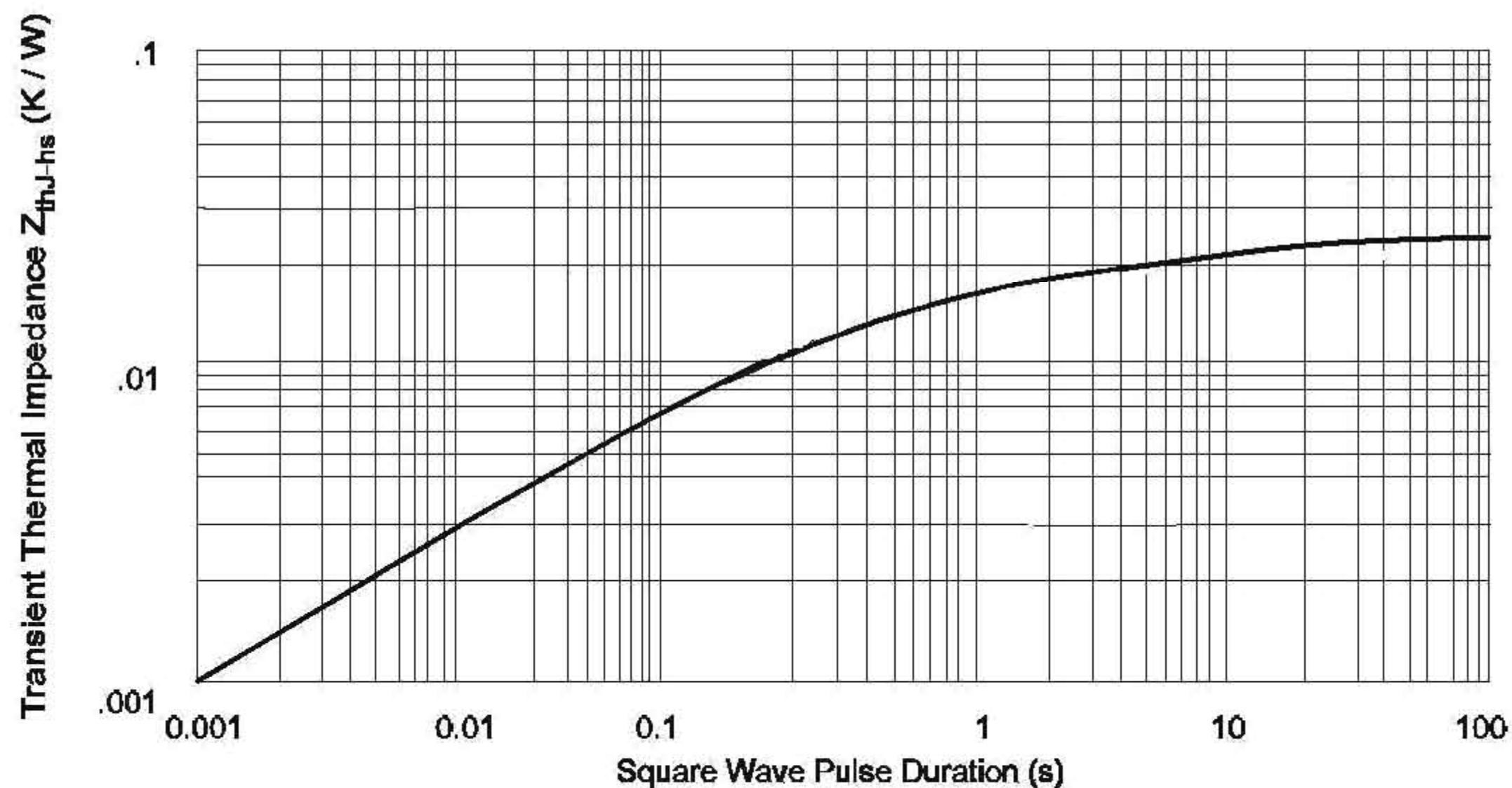


Fig. 10 Thermal Impedance Z_{thJ-hs} Characteristics

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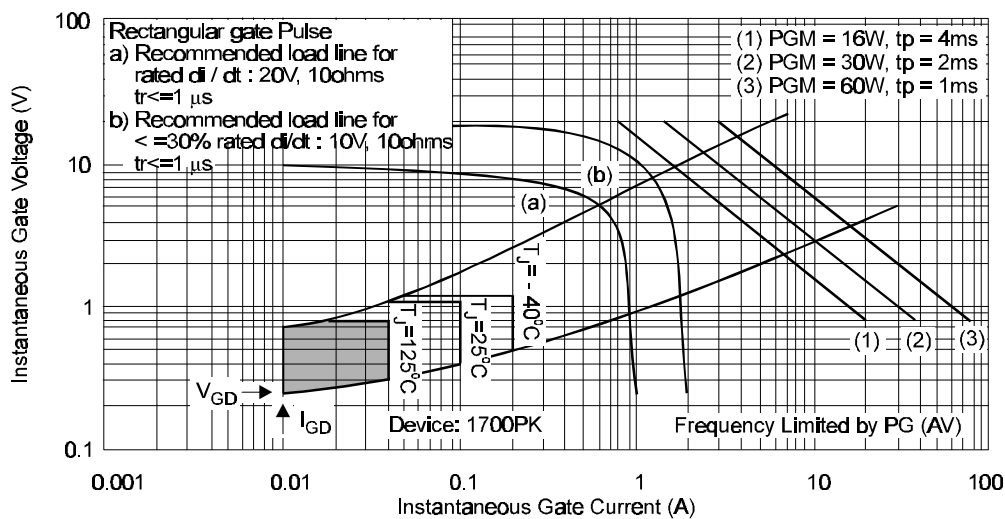
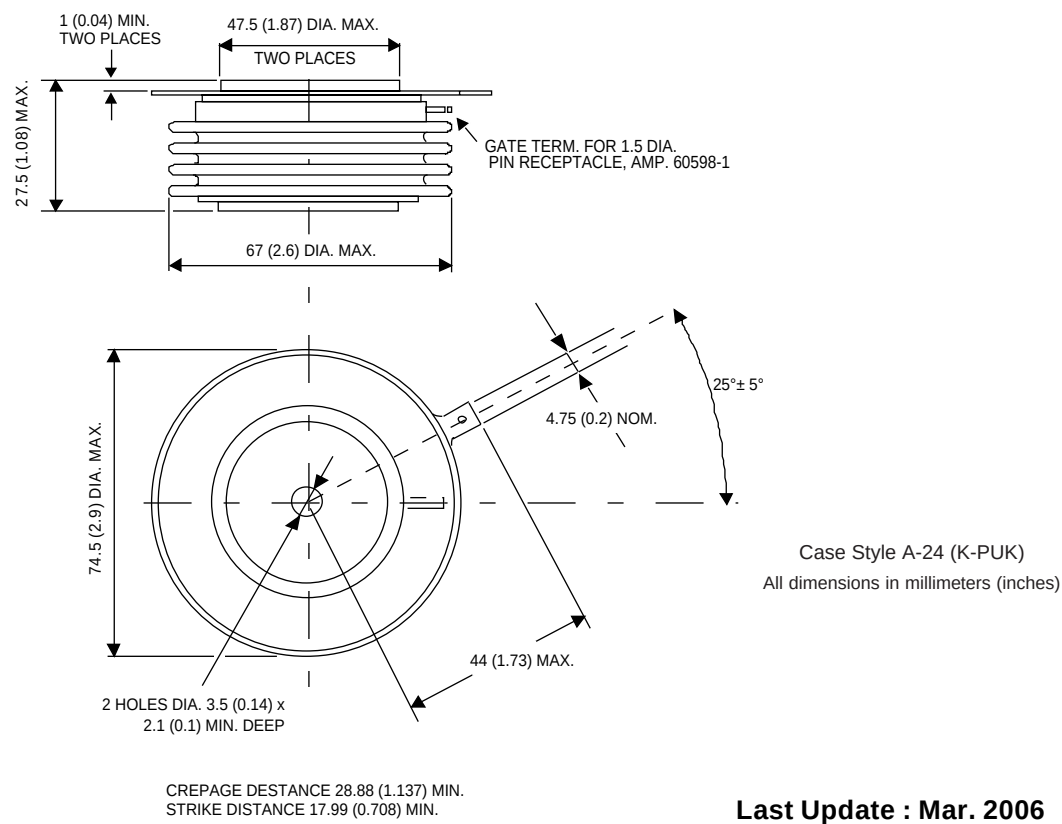


Fig.11 - Gate Characteristics

OUTLINE TABLE



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