



Ruttonsha International Rectifier Ltd.

SILICON CONTROLLED RECTIFIERS

High Power Thyristor Hockey Puk Version K-PUK Series 1950 PK

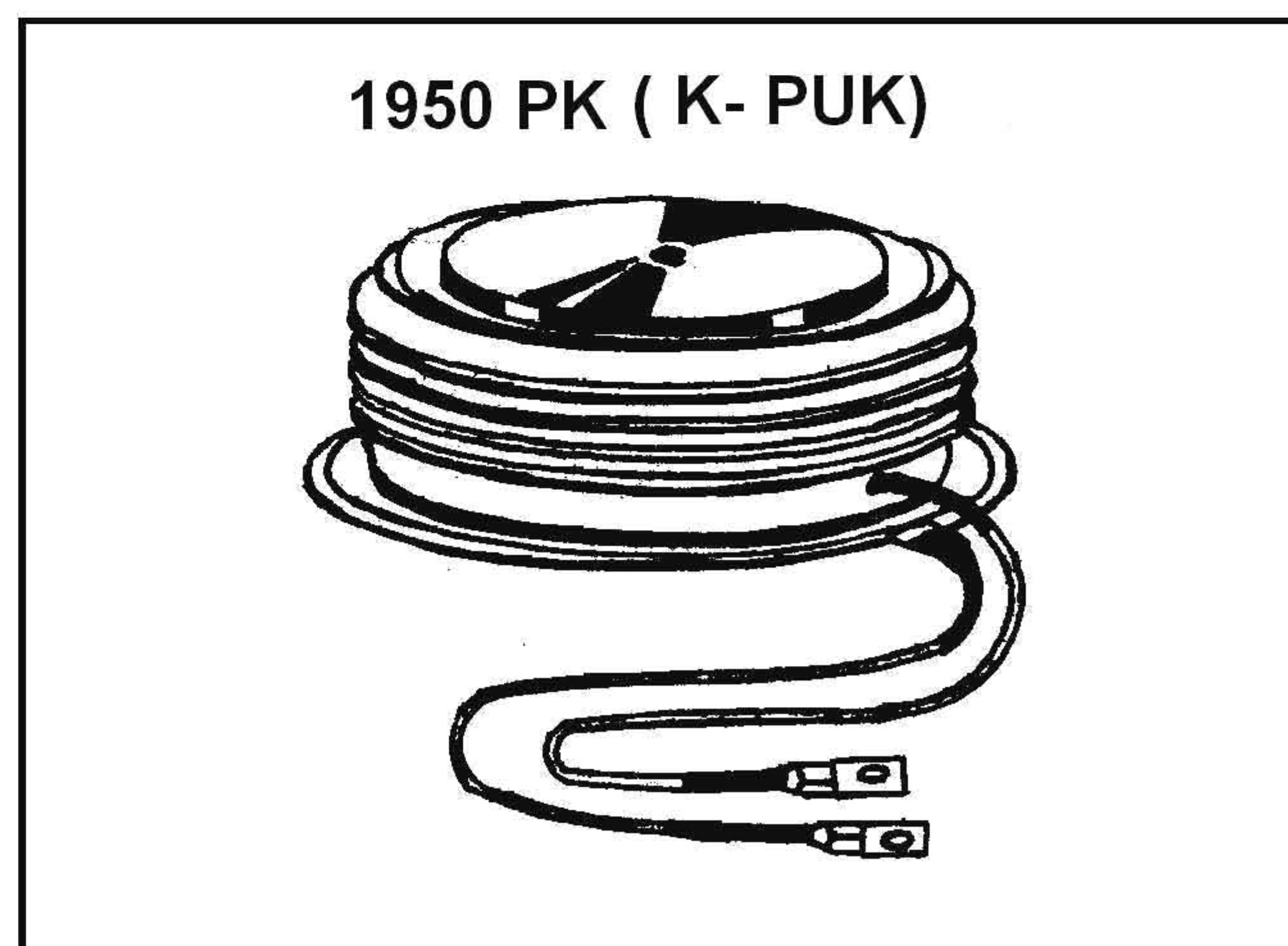
Types : 1950 PK 160

FEATURES

- ❖ Center amplifying gate.
- ❖ Metal case with ceramic insulator
- ❖ High profile hockey - puk.

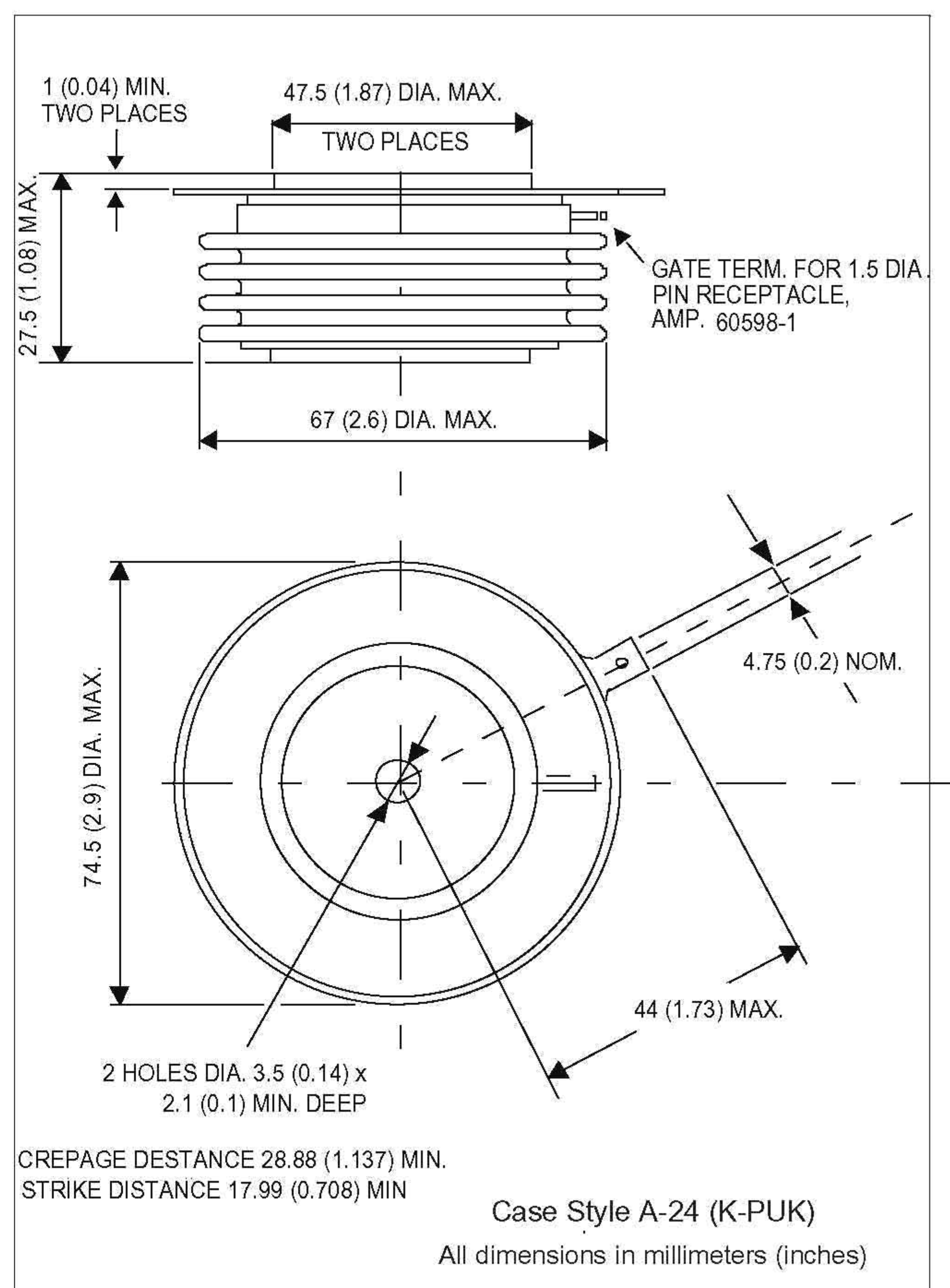
TYPICAL APPLICATIONS

- ❖ DC motor control (e.g. for machine tools).
- ❖ Controlled rectifiers (e.g. for battery charging, Uninterrupted Power Supply).
- ❖ AC controllers (e.g. for temperature control, lights control).



Major Ratings and Characteristics

Parameter	1950 PK	Units
$I_{T(AV)}$	1950	A
@ T_{hs}	55	°C
$I_{T(RMS)}$	3062	A
@ T_{hs}	55	°C
I_{TSM}	@ 50 Hz	26.25 KA
I^2t	@ 50 Hz	3450 KA ² s
V_{DRM}/V_{RRM}	800 to 1600	V
t_q	typical	200 μs
T_J	125	°C



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ELECTRICAL SPECIFICATIONS

1950 PK Series

Voltage Ratings

Type number	Voltage Code	V_{DRM}/V_{RRM} , max repetitive peak and off-state voltage V	V_{RSM} , maximum non-repetitive peak voltage V	I_{DRM}/I_{RRM} max. @ $T_J = T_J$ max. mA
1950 PK	80	800/800	900	100
	100	1000/1000	1100	
	120	1200/1200	1300	
	140	1400/1400	1500	
	160	1600/1600	1700	

On - state Conduction

Parameter	1950 PK	Units	Conditions
$I_{T(AV)}$ Max. average on-state current @ Heatsink temperature	1950	A	180° conduction, half sine wave
	55	°C	double side cooled
$I_{T(RMS)}$ Max RMS on-state current	3062	A	DC @ 55°C heatsink temperature double side cooled
I_{TSM} Max. peak, one-cycle non-repetitive surge current	26.25	KA	$t = 10$ ms
			Sinusoidal half wave, Initial $T_J = T_J$ max.
I^2t Maximum I^2t for fusing	3450	KA ² s	$t = 10$ ms
$V_{T(TO)}$ Threshold voltage	0.94	V	$T_J = T_J$ max
r_t on-state slope resistance	0.12	mΩ	$T_J = T_J$ max.
V_{TM} Max. on state voltage	1.40	V	$I_{PK} = 3000A, T_J = T_J$ max, $t_P = 10$ ms sine pulse
I_H Maximum holding current	150	mA	$T_J = 25^\circ C$, anode supply 12 V resistive load
I_L Typical latching current	500	mA	$T_J = 25^\circ C$, anode supply 12 V resistive load

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1950 PK Series

Switching

Parameter	1950 PK	Units	Conditions
di/dt Max. non-repetitive rate of rise of turned-on current	100	A/μs	Gate drive 20V, 20Ω, $t_r \leq 1\mu s$ $T_J = T_J$ max. anode voltage $\leq 80\% V_{DRM}$
t_q Typical turn-off time	200	μs	$I_{TM} = 1000A$, $T_J = T_J$ max. di/dt = 40A/μs, $V_R = 75V$ dv/dt = 50V/μs, 0.5 V_{DRM} Reapplied, $t_p = 500\mu s$

Blocking

Parameter	1950 PK	Units	Conditions
dv/dt Maximum critical rate of rise of off-state voltage	500	V/μs	$T_J = T_J$ max. linear to 80% rated V_{DRM}
I_{RRM} I_{DRM} Max. peak reverse and off-state leakage current	100	mA	$T_J = T_J$ max. rated V_{DRM} / V_{RRM} applied

Triggering

Parameter	1950 PK	Units	Conditions
P_{GM} Maximum peak gate power	16	W	$T_J = T_J$ max., $t_p \leq 5$ ms
$P_{G(AV)}$ Maximum average gate power	3.0		$T_J = T_J$ max., $f = 50Hz$, $d\% = 50$
I_{GM} Max. peak positive gate current	3.0	A	$T_J = T_J$ max., $t_p \leq 5$ ms
$+V_{GM}$ Maximum peak positive gate voltage	20	V	$T_J = T_J$ max., $t_p \leq 5$ ms
$-V_{GM}$ Maximum peak negative gate voltage	5.0		
I_{GT} DC gate current required to trigger	250	mA	$T_J = 25^\circ C$ Max.required gate trigger/ current/voltage are the lowest value which will trigger all units 12 V anode-to-cathode applied
V_{GT} DC gate voltage required to trigger	3.0	V	$T_J = 25^\circ C$
I_{GD} DC gate current not to trigger	10	mA	$T_J = T_J$ max. Max. gate current/voltage not to trigger is the max. value which will not trigger any unit with rated V_{DRM} anode-to-cathode applied
V_{GD} DC gate voltage not to trigger	0.25	V	

PHASE CONTROL THYRISTORS

1950 PK Series

Thermal and Mechanical Specifications

Parameter		1950 PK	Units	Conditions
T _J	Max.operating temperature range	125	°C	
T _{stg}	Max.storage temperature range	140		
R _{thJ-hs}	Max. thermal resistance, junction to heatsink	0.024	K/W	DC operation double side cooled
F	Mounting force, ± 10%	24.5	KN	
wt	Approximate weight	425	g	
Case style		K-PUK		See Outline Table