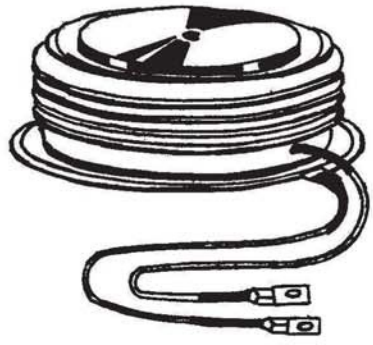
Ruttonsha International Rectifier Ltd.

SILICON CONTROLLED RECTIFIERS

High Power Thyristor
Hockey Puk Version
S/SS-PUK Series 5500PS/PSS

Types : 5500 PS/PSS

5500 (S/SS-PUK)



FEATURES

- *Center amplifying gate.*
- *Metal case with ceramic insulator*
- *High profile hockey - puk.*

TYPICAL APPLICATIONS

- *DC motor control (e.g. for machine tools).*
- *Controlled rectifiers (e.g. for battery charging, Uninterrupted Power Supply).*
- *AC controllers (e.g. for temperature control, lights control).*

Major Ratings and Characteristics

Parameter		5500PS/PSS	Units
$I_{T(AV)}$		5500	A
	@ T_{hs}	70	°C
$I_{T(RMS)}$		8635	A
	@ T_{hs}	70	°C
I_{TSM}	@ 50 Hz	75	KA
I^2t	@ 50 Hz	28125	KA^2s
V_{DRM}/V_{RRM}		1600 to 3600	V
t_q	typical	400	µs
T_J		125	°C

ELECTRICAL SPECIFICATIONS 5500PS/PSS Series

Voltage Ratings

Type number	Voltage Code	V_{DRM}/V_{RRM}, max repetitive peak and off-state voltage V	V_{RSM},maximum non-repetitive peak voltage V	I_{DRM}/I_{RRM} max. @ $T_J = T_J$ max. mA
5500PS/PSS	160	1600 / 1600	1700	400
	200	2000 / 2000	2100	
	260	2600 / 2600	2700	
	360	3600 / 3600	3700	

On - state Conduction

	Parameter	5500PS/PSS	Units	Conditions	
$I_{T(AV)}$	Max. average on-state current	5500	A	180° conduction, half sine wave	
	@ Heatsink temperature	70	°C	double side cooled	
$I_{T(RMS)}$	Max RMS on-state current	8635	A	DC @ 70°C heatsink temperature double side cooled	
I_{TSM}	Max. peak, one-cycle non-repetitive surge current	75	KA	t = 10 ms	Sinusoidal half wave, Initial $T_J=T_J$ max.
I^2t	Maximum I^2t for fusing	28125	KA^2s	t = 10 ms	
$V_{T(TO)}$	Threshold voltage	0.86	V	$T_J=T_J$ max	
r_t	On-state slope resistance	0.073	mΩ	$T_J=T_J$ max.	
V_{TM}	Max. on state voltage	1.3	V	I_{PK} = 4000A,$T_J=T_J$ max, t_P = 10 ms sine pulse	
I_H	Maximum holding current	600	mA	T_J = 25°C , anode supply 12 V resistive load	
I_L	Typical latching current	1000	mA	T_J = 25°C , anode supply 12 V resistive load	

5500PS/PSS Series

Switching

	Parameter	5500PS/PSS	Units	Conditions
di/dt	Max. non-repetitive rate of rise of turned-on current	300	A/µs	Gate drive 20V, 20Ω , $t_r \leq 1\mu s$ $T_J=T_J$ max. anode voltage ≤ 80% V_{DRM}
t_q	Typical turn-off time	400	µs	I_{TM} =1000A, $T_J=T_J$ max. di/dt = 5 A/µs ,V_R =100V dv/dt = 20V/µs, V_{DRM} 2000V, t_p = 500µs

Blocking

	Parameter	5500PS/PSS	Units	Conditions
dv/dt	Maximum critical rate of rise of off-state voltage	1000	V/µs	$T_J = T_J$ max. linear to 67% rated V_{DRM}
I_{RRM} I_{DRM}	Max. peak reverse and off-state leakage current	400	mA	$T_J = T_J$ max. rated V_{DRM} /V_{RRM} applied

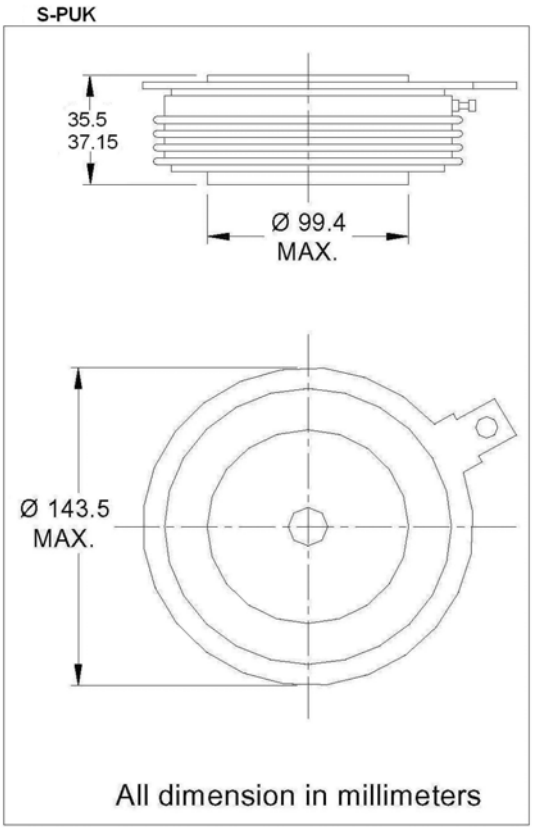
Triggering

	Parameter	5500PS/PSS	Units	Conditions
P_{GM}	Maximum peak gate power	50	W	$T_J = T_J$ max., $t_p \leq 5$ ms
$P_{G(AV)}$	Maximum average gate power	5		$T_J = T_J$ max., f = 50Hz, d% = 50
I_{GM}	Max. peak positive gate current	3.0	A	$T_J = T_J$ max., $t_p \leq 5$ ms
$+V_{GM}$	Maximum peak positive gate voltage	20	V	$T_J = T_J$ max., $t_p \leq 5$ ms
$-V_{GM}$	Maximum peak negative gate voltage	5.0		
		MAX.		
I_{GT}	DC gate current required to trigger	300	mA	T_J = 25°C Max.required gate trigger/ current/voltage are the lowest value which will trigger all units 12 V anode-to-cathode applied
V_{GT}	DC gate voltage required to trigger	5.0	V	T_J = 25°C
I_{GD}	DC gate current not to trigger	15	mA	$T_J = T_J$ max. Max. gate current/voltage not to trigger is the max. value which will not trigger any unit with rated V_{DRM} anode-to-cathode applied
V_{GD}	DC gate voltage not to trigger	0.25	V	

Thermal and Mechanical Specifications

	Parameter	5500PS/PSS	Units	Conditions
T_J	Max.operating temperature range	125	°C	
T_{stg}	Max.storage temperature range	150		
R_{thJ-C}	Max. thermal resistance, junction to case	0.008	K/W	DC operation double side cooled
F	Mounting force, ± 10% ,	80 to 90	KN	
wt.	Approximate weight	3000	g	
	Case style	S/SS-PUK		See Outline Table

S-PUK



SS-PUK