RIR POWER ELECTRONICS LIMITED

SILICON CONTROLLED RECTIFIERS

High Power Thyristor
Hockey Puk Version
E-PUK Series 601PE

Types : 601PE 150 to 601PE 300

601PE ...

FEATURES

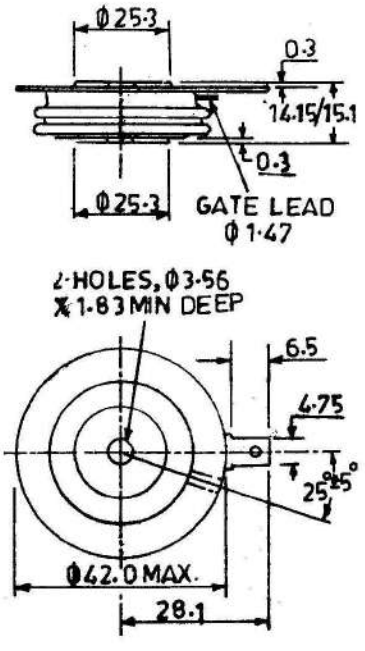
- *Center amplifying gate.*
- *International standard case TO-200AB (E-PUK)*
- *High profile hockey - puk.*

TYPICAL APPLICATIONS

- *DC motor control (e.g. for machine tools).*
- *Controlled rectifiers (e.g. for battery charging, UPS).*
- *AC controllers (e.g. for temperature control, lights control).*

MAJOR RATINGS & CHARACTERISTICS

Parameters		601PE	Units
$I_{T(AV)}$		601	A
	@ T_{hs}	55	^{0}C
$I_{T(RMS)}$		942	A
	@ T_{hs}	55	^{0}C
I_{TSM}	@ 50 Hz	7500	A
I^2t	@ 50 Hz	280	KA^{2}s
V_{DRM} / V_{RRM}		1500 to 3000	V
t_q	typical	250	µs
T_J		-40 to 125	^{0}C



UNIT:- M M

SILICON CONTROLLED RECTIFIERS

601PE

ELECTRICAL SPECIFICATION

VOLTAGE RATINGS

Type Number	Voltage Code	V_{RRM} / V_{DRM}, max. repetitive peak and off-state voltage V	V_{RSM}, max. non-repetitive peak voltage V	I_{DRM} / I_{RRM} max. @ 125°C mA
601PE	150	1500	1600	50
	170	1700	1800	
	190	1900	2000	
	210	2100	2200	
	230	2300	2400	
	250	2500	2600	
	270	2700	2800	
	290	2900	3000	
	300	3000	3100	

ON-STATE CONDUCTION

	Parameter	601PE	Units	Conditions
$I_{T(AV)}$	Max. average on-state current	601	A	180° conduction, half sine wave
	@ heat sink temperature	55	°C	double side cooled
$I_{T(RMS)}$	Max. RMS on-state current	942	A	@55°C heat sink temperature (double side cooled)
I_{TSM}	Max. peak one cycle non-repetitive surge current	7500	A	t = 10ms; Sinusodial half wave, Initial T_J = T_J max.
I^2t	Maximum I^2t for fusing	280	kA^2s	t = 10ms; Sinusodial half wave, Initial T_J = T_J max.
V_{TO}	Threshold voltage	1.14	V	T_J = T_J max.
r_t	On state slope resistance	0.78	mΩ	T_J = T_J max.
V_{TM}	Max. on state voltage	2.1	V	I_{pk} = 1500 A, T_J = 125°C, t_p = 10ms sine pulse
I_H	Maximum holding current	600	mA	T_J = 25°C, anode supply 12V resistive load
I_L	Latching current	1000		

SWITCHING

	Parameter	601PE	Units	Conditions
di/dt	Max. non-repetitive rate of rise of turned-on current	500	A/µs	Gate drive 20V, 20Ω, tr ≤ 1 µs T_J = 125°C, anode voltage ≤ 80% V_{DRM}
t_d	Typical delay time	1.0	µs	Gate current 1A, di_g/dt = 1A/µs V_d = 0.67% V_{DRM}, T_J = 25°C
t_q	Typical turn-off time	250	µs	I_{TM} = 550A, T_J = 125°C, di/dt = 40A/µs, V_R = 50V dv/dt = 20V/µs, Gate 0V 100Ω, t_p = 500µs

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BLOCKING

	Parameter	601PE	Units	Conditions
dv/dt	Maximum critical rate of rise of off-state voltage	1000	V/µs	T_J = 125°C, linear to 80% rated V_{DRM}
I_{RRM} I_{DRM}	Max. peak reverse and off-state leakage current	50	mA	T_J = 125°C, rated V_{DRM} /V_{RRM} applied

TRIGGERING

	Parameter	601PE		Units	Conditions	
P_{GM}	Maximum peak gate power	10.0		W	T_J = 125°C, $t_p \leq$ 5ms	
$P_{G(AV)}$	Maximum average gate power	2.0			T_J = 125°C, f= 50Hz, d% = 50	
I_{GM}	Max. peak positive gate current	3.0		A	T_J = 125°C, $t_p \leq$ 5ms	
$+V_{GM}$	Max. peak positive gate voltage	20		V	T_J = 125°C, $t_p \leq$ 5ms	
$-V_{GM}$	Max. peak negative gate voltage	5.0				
I_{GT}	DC gate current required to trigger	TYP. 100	MAX. 200	mA	T_J = 25°C	Max. required gate trigger/current / voltage are the lowest value which will trigger all units 12V anode-to-cathode applied.
V_{GT}	DC gate voltage required to trigger	1.8	3.0	V	T_J = 25°C	
I_{GD}	DC gate current not to trigger	10		mA	T_J = 125°C	Max. gate current / voltage not to trigger is the max. value which will not trigger any unit with rated V_{DRM} anode-to-cathode applied.
V_{GD}	DC gate voltage not to trigger	0.25		V		

THERMAL AND MECHANICAL SPECIFICATION

	Parameter	601PE	Units	Conditions
T_J	Max. operating temperature range	-40 to 125	°C	
T_{stg}	Max. storage temperature range	-40 to 150		
$R_{thJ\text{-}hs}$	Max. thermal resistance, junction to heat sink	0.05	K/W	DC operation double side cooled
F	Mounting force, ±10%	9800 (1000)	N (kg)	
wt	Approximate weight	83	g	
	Case style	To - 200AB (E-PUK)		See outline