Ruttonsha International Rectifier Ltd.

PHASE CONTROL THYRISTORS

HOCKEY PUCK VERSION

Type : 900 PB 120 To 220

910 A

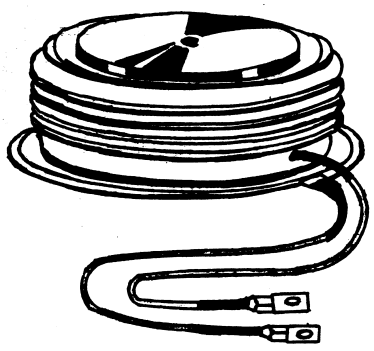
Features

- Center amplifying gate
- Metal case with ceramic insulator
- International standard case (B-PUK)
- High profile hockey-puk

Typical Applications

- D C motor controls
- Controlled D C power supplies
- A C controllers

900 PB (B - PUK)



Major Ratings and Characteristics :-

PARAMETERS		900 PB	UNITS
$I_{T(AV)}$		910	A
	@T_{hs}	55	°C
$I_{T(RMS)}$		1857	A
	@T_{hs}	25	°C
I_{TSM}	@50Hz	15700	A
I^2t	@50Hz	1232	KA^2s
V_{DRM}	/ V_{RRM}	1200 to 2200	V
T_q	typical	150	µs
T_J		- 40 to 125	°C

ELECTRICAL SPECIFICATIONS 900 PB Series

Voltage Ratings

Type number	Voltage Code	V_{DRM}/V_{RRM}, max repetitive peak and off-state voltage V	V_{RSM},maximum non-repetitive peak voltage V	I_{DRM}/I_{RRM} max. @ $T_J = T_J$ max. mA
900 PB	120	1200	1300	80
	160	1600	1700	
	180	1800	1900	
	200	2000	2100	
	220	2200	2300	

On - state Conduction

Parameter		900 PB	Units	Conditions		
$I_{T(AV)}$	Max. average on-state current	910(355)	A	180° conduction, half sine wave		
	@ Heatsink temperature	55 (85)	°C	double side (single side) cooled		
$I_{T(RMS)}$	Max RMS on-state current	1857		DC@25°C heatsink temperature double side cooled		
I_{TSM}	Max. peak, one-cycle non-repetitive surge current	15700	A	t = 10 ms	No voltage reapplied	Sinusoidal half wave, Initial $T_J=T_J$ max.
I^2t	Maximum I^2t for fusing	1232	KA^2s	t = 10 ms	No voltage reapplied	
$V_{T(TO)}$	Threshold voltage	1.00	V	$T_J=T_J$ max		
r_t	On-state slope resistance	0.66	mΩ	$T_J=T_J$ max.		
V_{TM}	Max. on state voltage	1.80	V	I_{PK} = 2000A,$T_J=T_J$ max, t_P = 10 ms sine pulse		
I_H	Maximum holding current	600	mA	T_J = 25°C , anode supply 12 V resistive load		
I_L	Typical latching current	1000				

900 PB Series

Switching

	Parameter	900 PB	Units	Conditions
di/dt	Max. non-repetitive rate of rise of turned-on current	100	A/µs	Gate drive 20V, 20Ω , $t_r \leq 1\mu s$ $T_J = T_J$ max. anode voltage ≤ 80% V_{DRM}
t_d	Typical delay time	1.0	µs	Gate current 1A, di_g/dt = 1A/µs V_d = 0.67% V_{DRM} , T_J = 25° C
t_q	Typical turn-off time	150		I_{TM} = 750A, $T_J = T_J$ max. di/dt = 60A/µs , V_R = 50V dv/dt = 20V/µs , Gate OV 100 Ω, t_p = 500µs

Blocking

	Parameter	900 PB	Units	Conditions
dv/dt	Maximum critical rate of rise of off-state voltage	500	V/µs	$T_J = T_J$ max. linear to 80% rated V_{DRM}
I_{RRM} I_{DRM}	Max. peak reverse and off-state leakage current	80	mA	$T_J = T_J$ max. rated V_{DRM} /V_{RRM} applied

Triggering

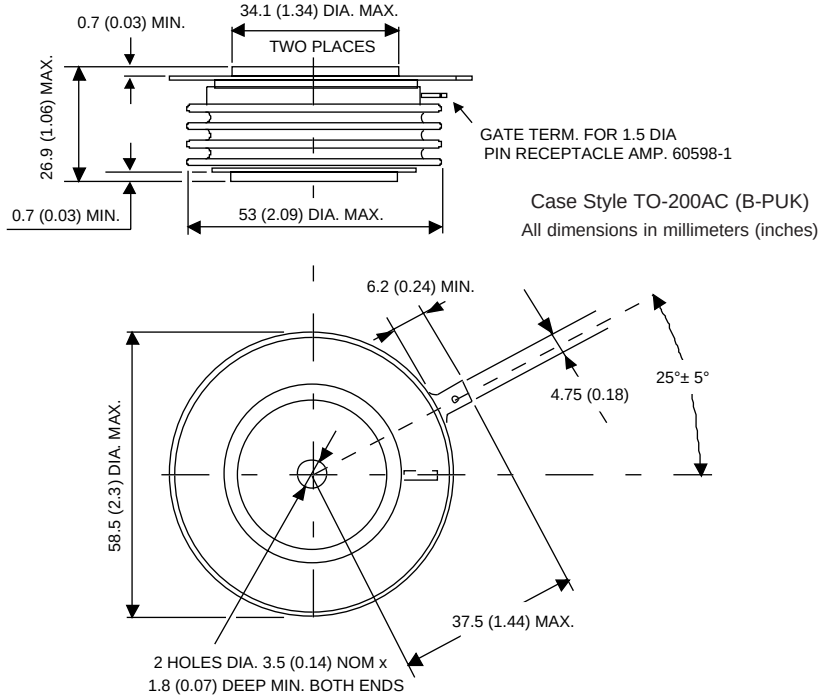
	Parameter	900 PB	Units	Conditions
P_{GM}	Maximum peak gate power	10.0	W	$T_J = T_J$ max., $t_p \leq 5$ ms
$P_{G(AV)}$	Maximum average gate power	2.0		$T_J = T_J$ max., f = 50Hz, d% = 50
I_{GM}	Max. peak positive gate current	3.0	A	$T_J = T_J$ max., $t_p \leq 5$ ms
$+V_{GM}$	Maximum peak positive gate voltage	20	V	$T_J = T_J$ max., $t_p \leq 5$ ms
$-V_{GM}$	Maximum peak negative gate voltage	5.0		
I_{GT}	DC gate voltage required to trigger	200	mA	T_J = 25°C Max.required gate trigger/ current/voltage are the lowest value which will trigger all units 12 V anode-to-cathode applied
V_{GT}	DC gate voltage required to trigger	3.0	V	T_J = 25°C
I_{GD}	DC gate current not to trigger	10	mA	$T_J = T_J$ max. Max. gate current/voltage not to trigger is the max. value which will not trigger any unit with rated V_{DRM} anode-to-cathode applied
V_{GD}	DC gate voltage not to trigger	0.25	V	

Thermal and Mechanical Specification

	Parameter	900 PB	Units	Conditions
T_J	Max.operating temperature range	- 40 to 125	°C	
T_{stg}	Max.storage temperature range	- 40 to 150		
$R_{thJ\text{-}hs}$	Max. thermal resistance, junction to heatsink	0.073 0.031	K/W	DC operation single side cooled DC operation double side cooled
$R_{thC\text{-}hs}$	Max. thermal resistance, case to heatsink	0.011 0.006	K/W	DC operation single side cooled DC operation double side cooled
F	Mounting force, ± 10%	14700 (1500)	N (Kg.)	
wt	Approximate weight	255	g	
	Case style	TO-200AC (B-PUK)		See Outline Table

900 PB Series

Outline Table



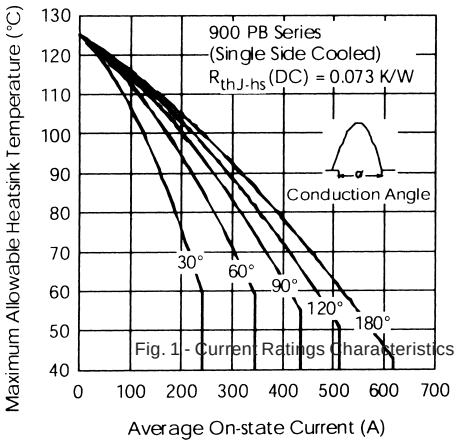


Fig. 1 - Current Ratings Characteristics

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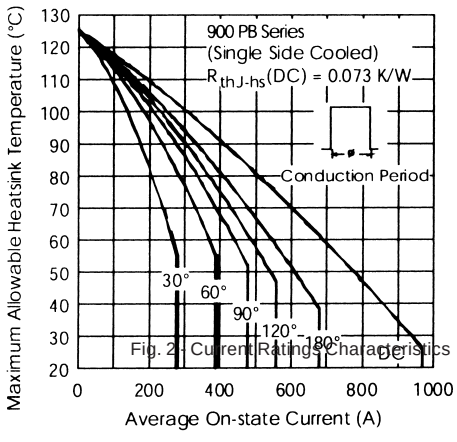

Fig. 2 - Current Ratings Characteristics

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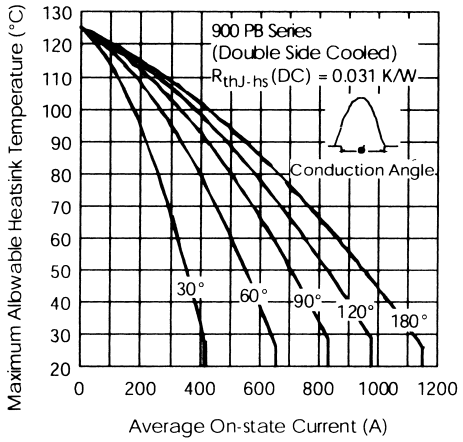

Fig. 3 - Current Ratings Characteristics

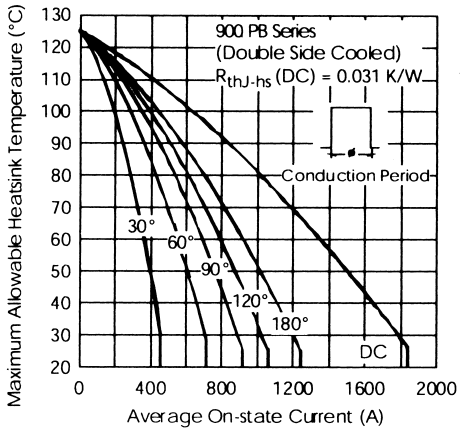

Fig. 4 - Current Ratings Characteristics

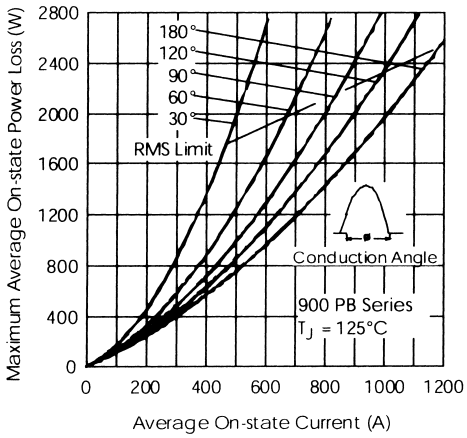

Fig. 5- On-state Power Loss Characteristics

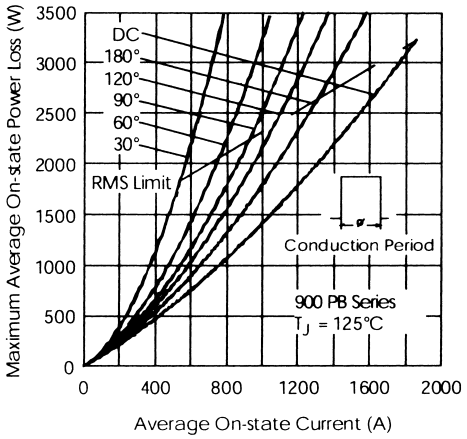

Fig. 6- On-state Power Loss Characteristics

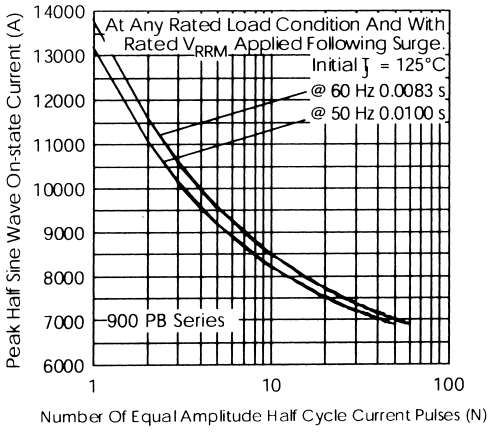

Fig. 7 - Maximum Non-Repetitive Surge Current
Single and Double Side Cooled

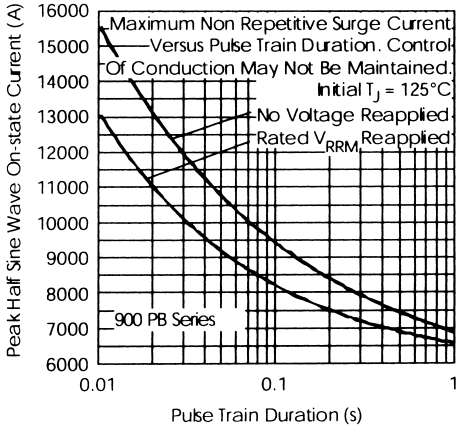

Fig. 8 - Maximum Non-Repetitive Surge Current
Single and Double Side Cooled

900 PB Series

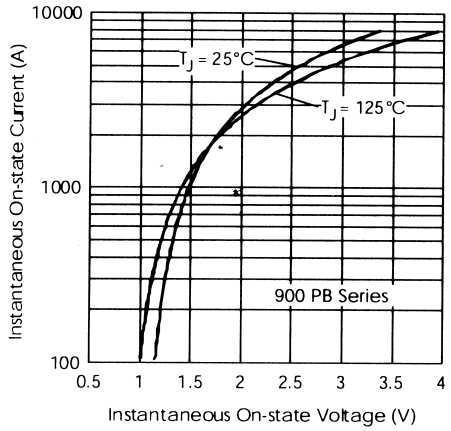

Fig. 9 - On-state Voltage Drop Characteristics

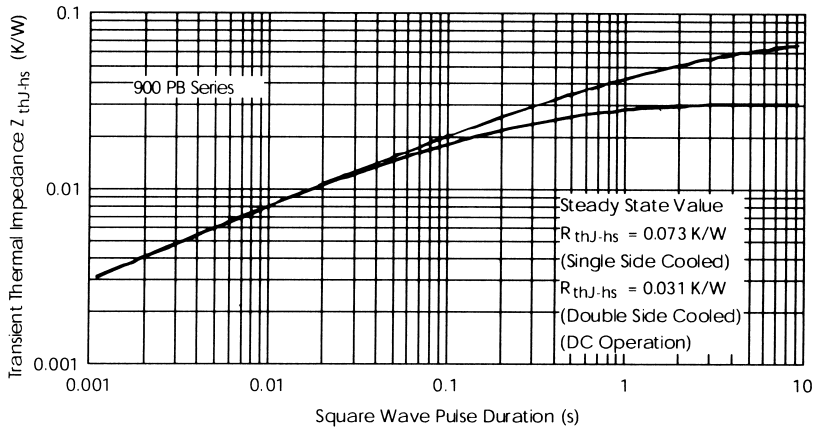

Fig. 10 - Thermal Impedance Z_{thJ-hs} Characteristics

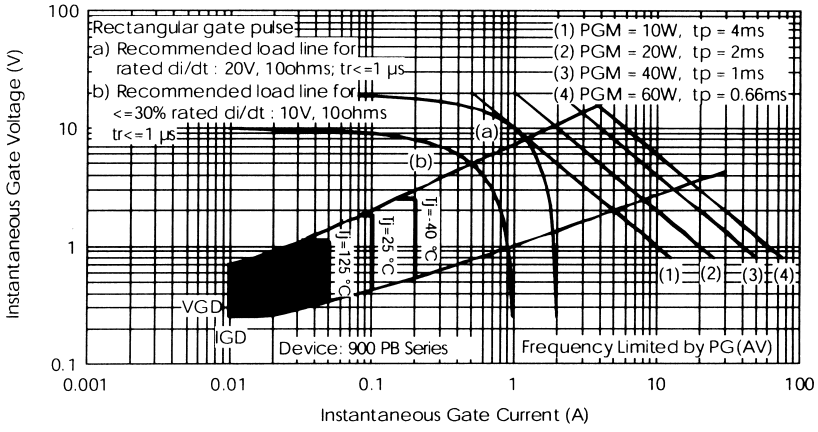

Fig. 11 - Gate Characteristics

In the interest of product improvement `R I R Ltd' reserves the right to change specification at any time without notice.
Latest Edition: February 2002